

SHEET

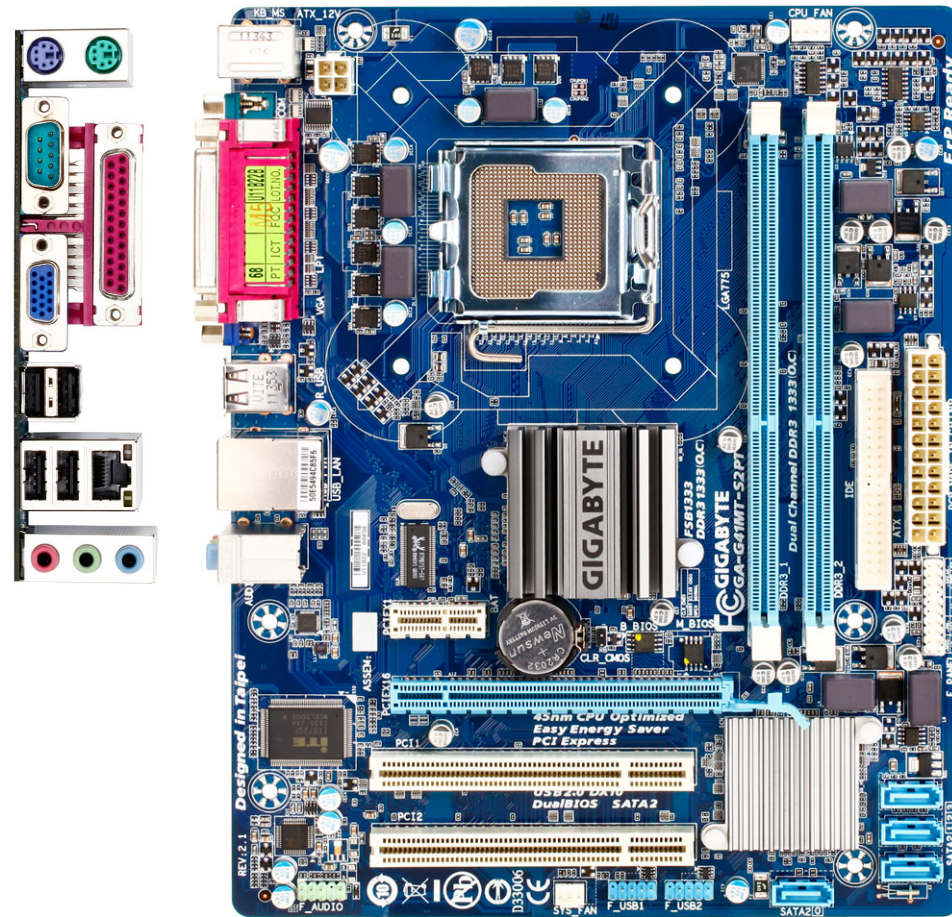
TITLE

SHEET

TITLE

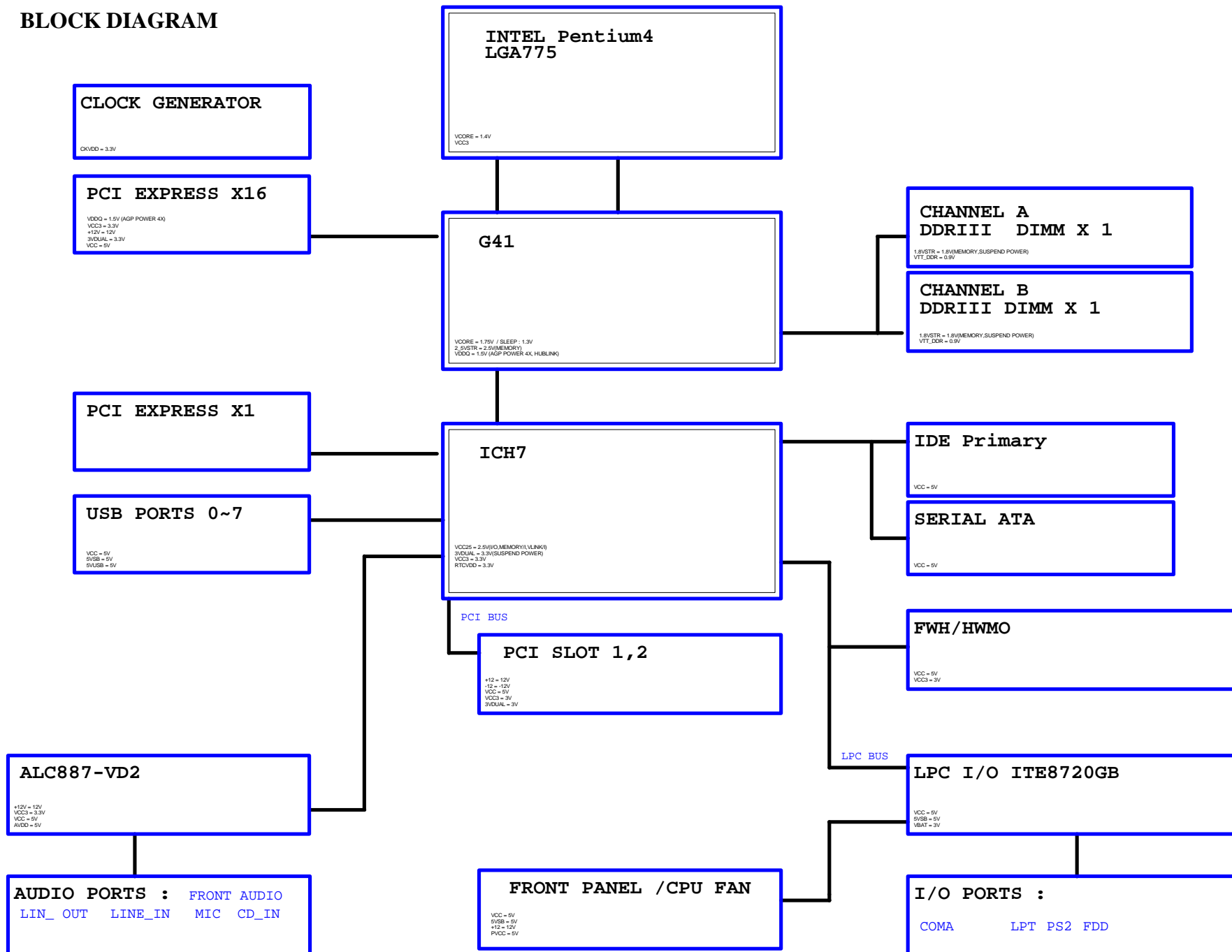
01	COVER SHEET
02	BLOCK DIAGRAM
03	BOM & PCB MODIFY HISTORY
04	P4_LGA775_A
05	P4_LGA775_B,D
06	P4_LGA775_C
07	P4_LGA775_E,F,G,H
08	G41_HOST
09	G41_DDRII
10	G41_PCI E, DMI
11	G41_VGA
12	G41_GND
13	G41_PWR
14	PCI EXPRESS*16 SLOT
15	DDR3 CHANNEL A
16	DDR3 CHANNEL B
17	DDR3 TERMINATION
18	ICH7 PCI, USB, DMI, LAN
19	ICH7 IDE, GPIO, SATA, CTRL
20	ICH7 VCC, GND
21	CK505 CLOCK.
22	PCI SLOT 1,2,PCIE*1
23	IDE
24	ITE 8720 GB
25	COM_LPT
26	CI ,HWM,KB/MS ,DUALBIOS
27	CODEC ALC887-VD2

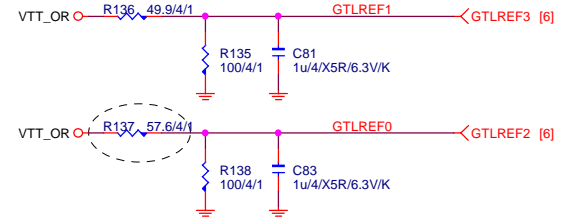
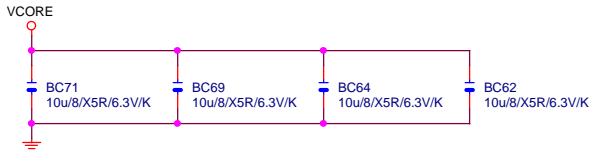
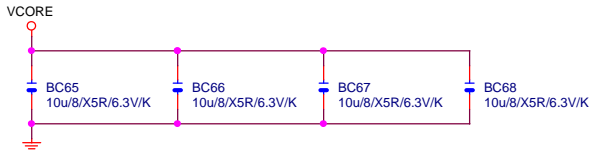
28	REAR AUDIO JACK
29	DISCRETE POWER
30	VCORE PWM_ISL6312
31	ATX, OTHERS POWER
32	FRONT PANEL
33	ARTHEROS AR8151



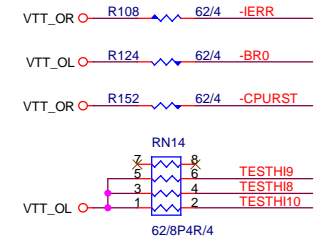
Gigabyte Technology		
Cover Sheet		
Title	GA-G41MT-S2PT	
Size	Document Number	Rev
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BLOCK DIAGRAM





中間値 0.9V

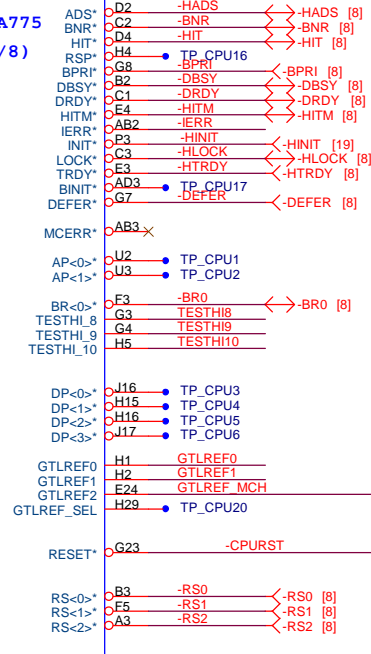
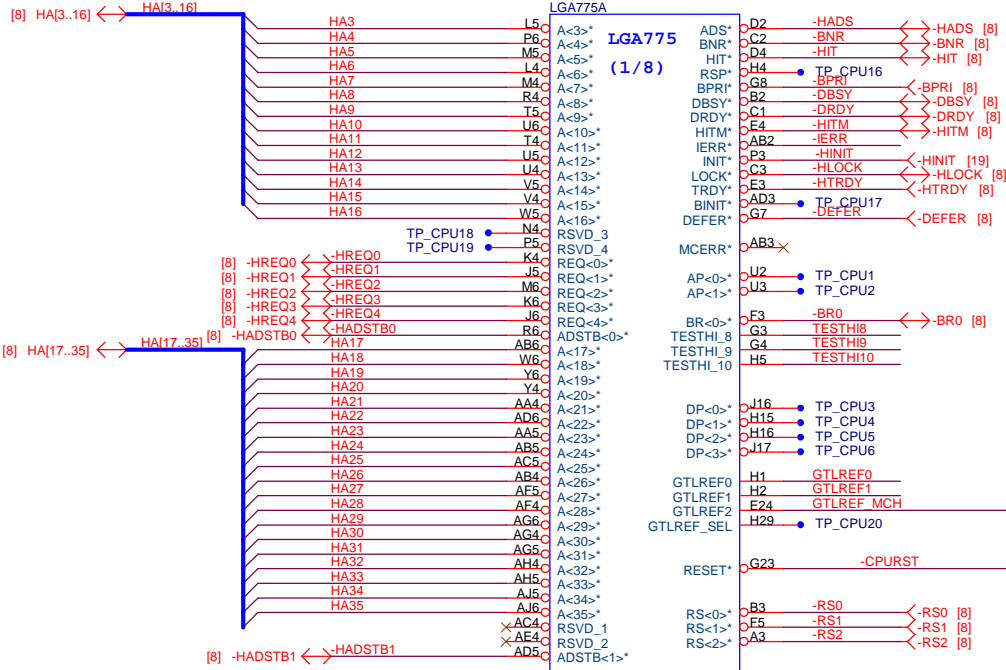


LGA775-39-2

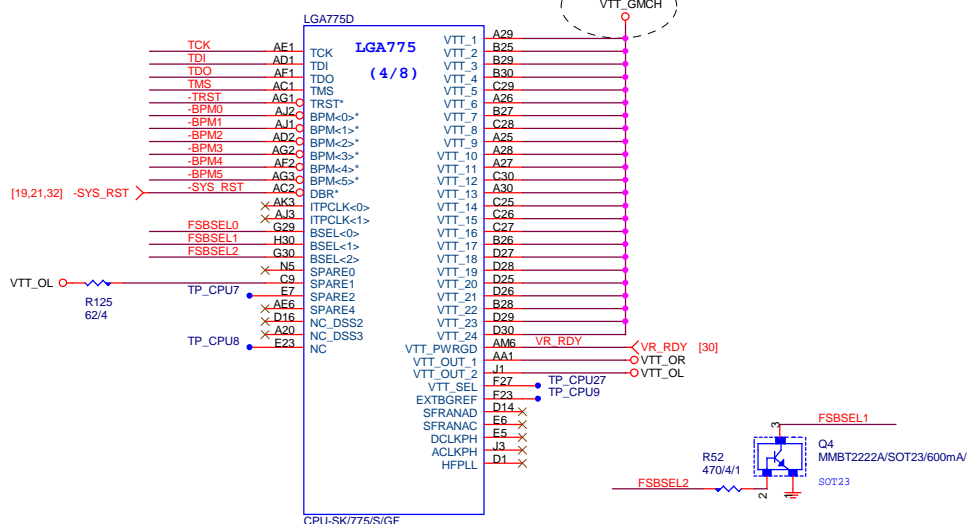
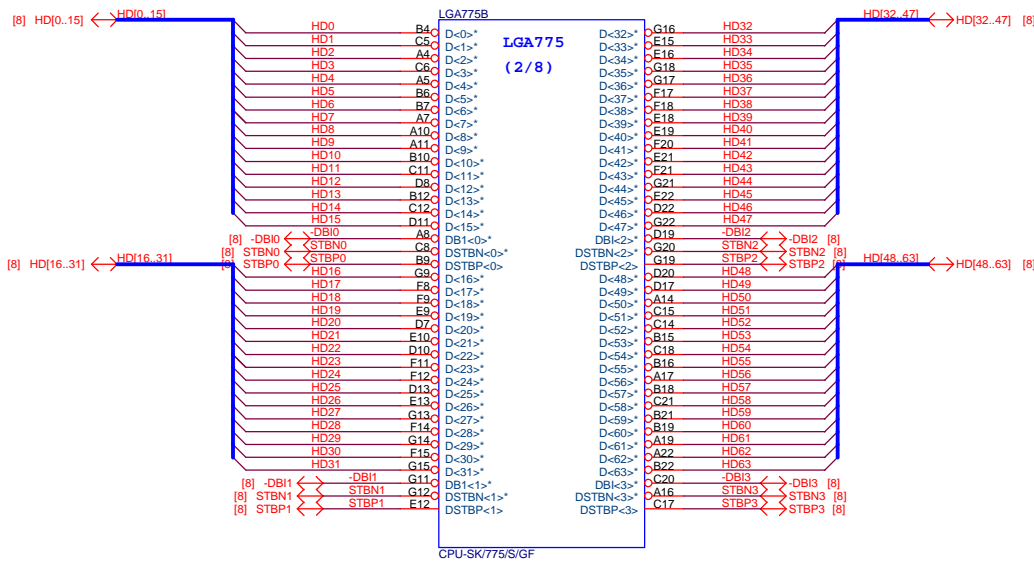
LGA775A

LGA775 (1/8)

CPU-SK775/S/GF

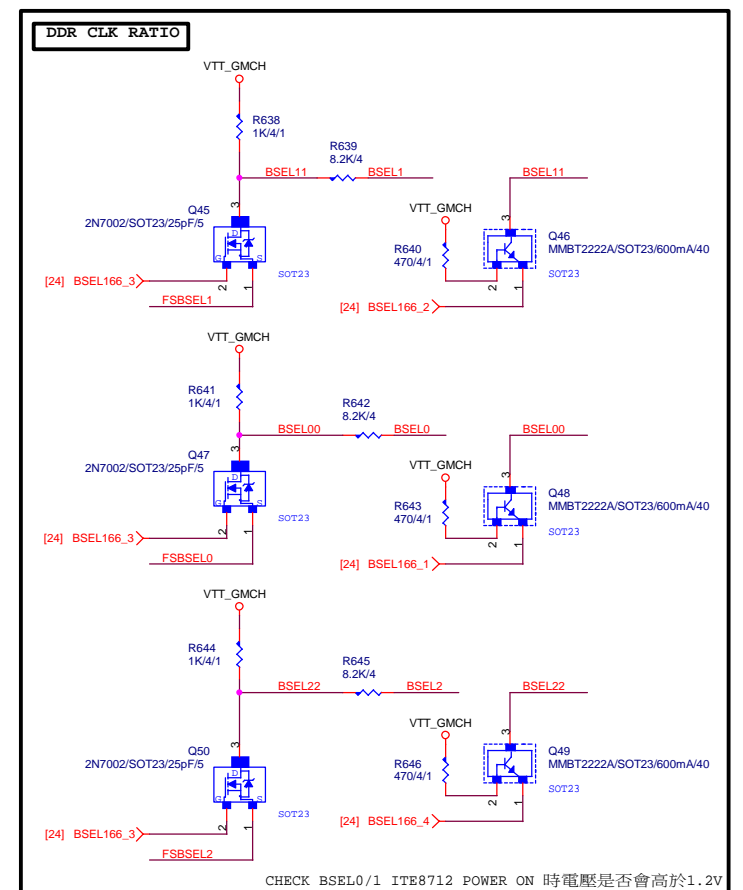
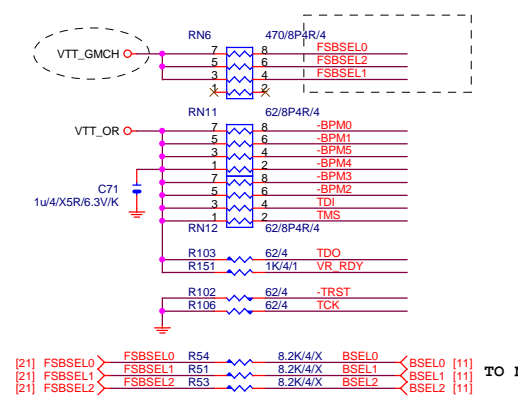


Gigabyte Technology			
Title P4_LGA775-A			
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FIX FSB1600 LATCH FAIL

	FSA	FSB	FSC	Clock	
?	1	0	1	100MHz	
?	1	0	0	133MHz	3/4
G33	0	1	0	200MHz	2/2.66/3.33/4+
G33	0	0	0	266MHz	2/2.5/3/4~
G33	0	0	1	333MHz	2/2.4/3.2/4#



CHECK BSEL0/1 ITE8712 POWER ON 時電壓是否會高於1.2V

Gigabyte Technology

Title: P4_LGA775-B,D

Size: Custom

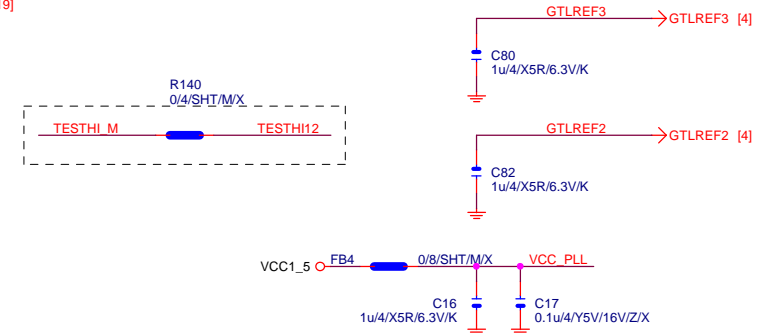
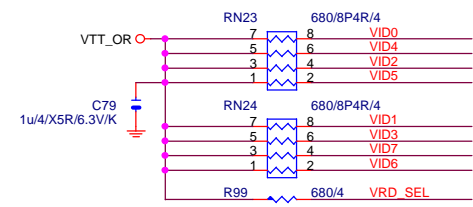
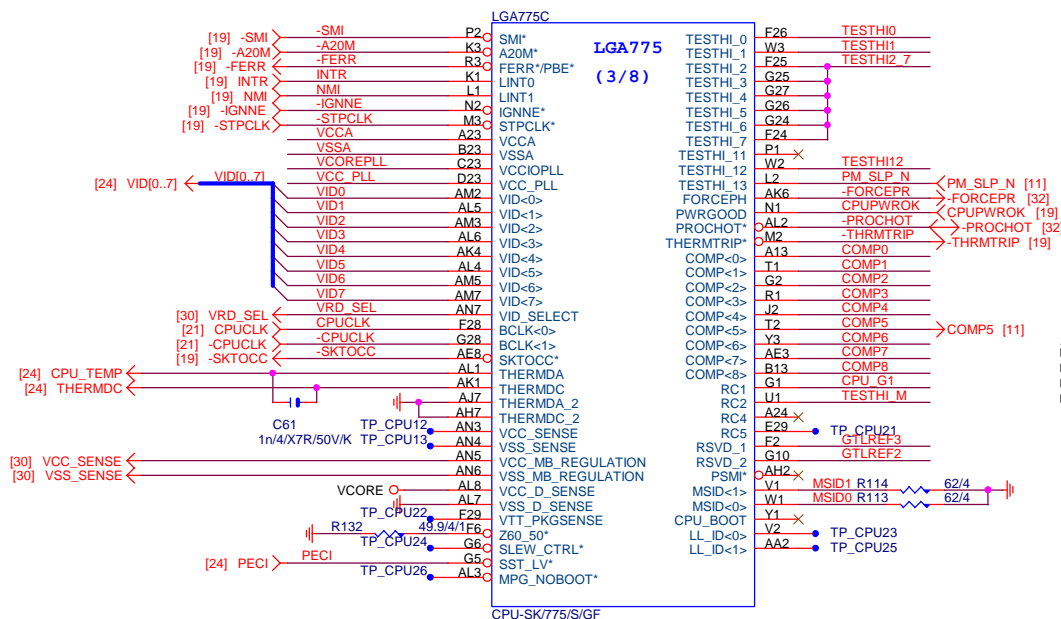
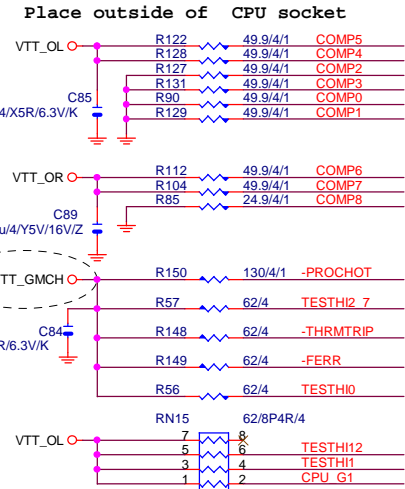
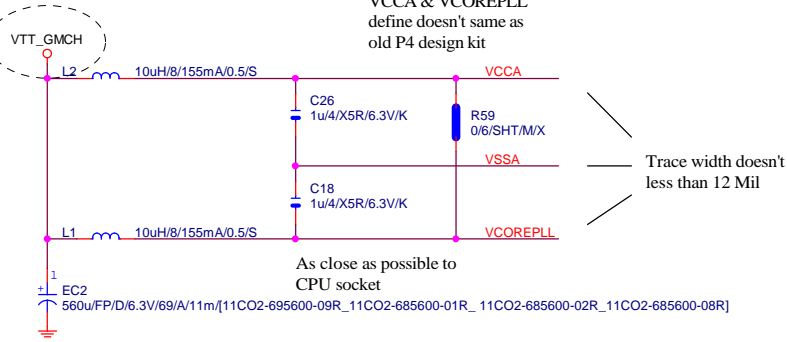
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Note:
VCCA & VCOREPLL
define doesn't same as
old P4 design kit



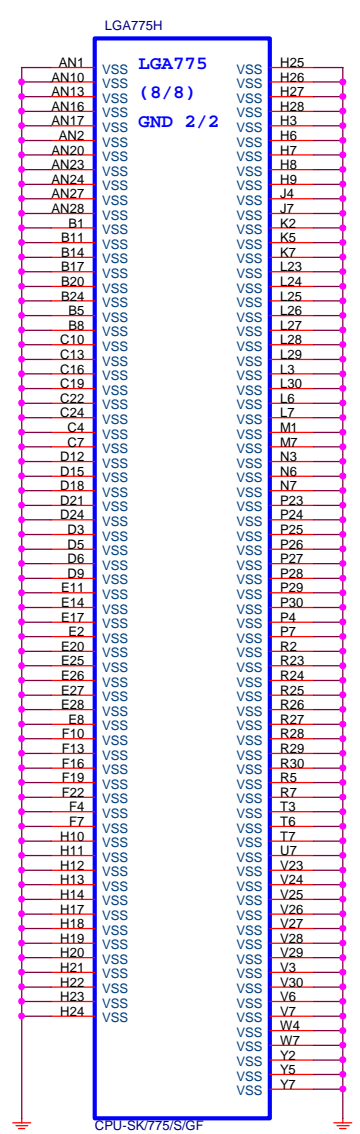
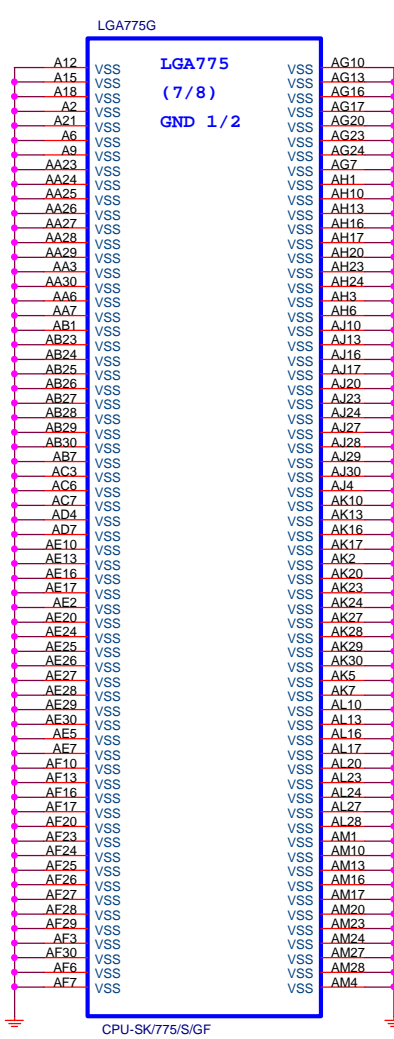
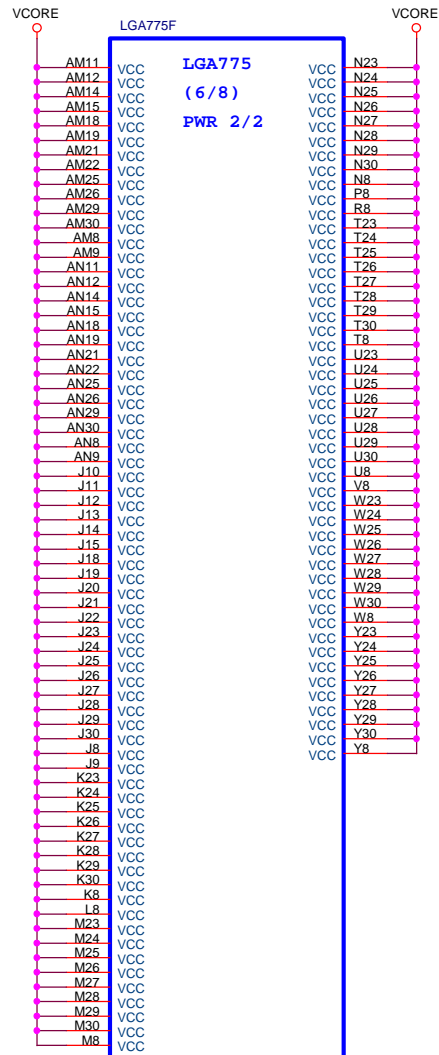
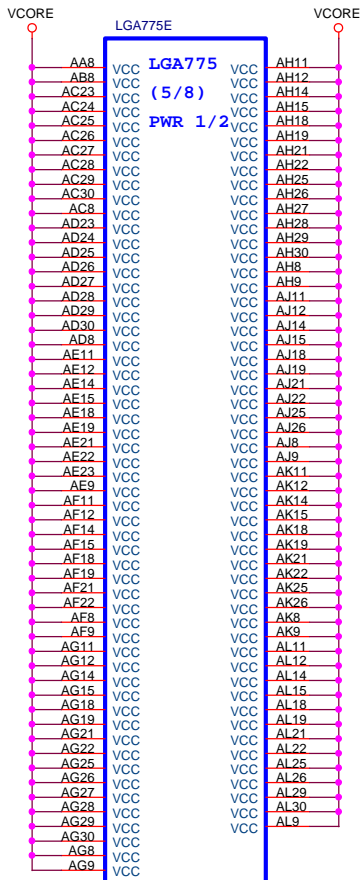
Gigabyte Technology

Title
P4_LGA775-C

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PECI:Platform Environment Control Interface

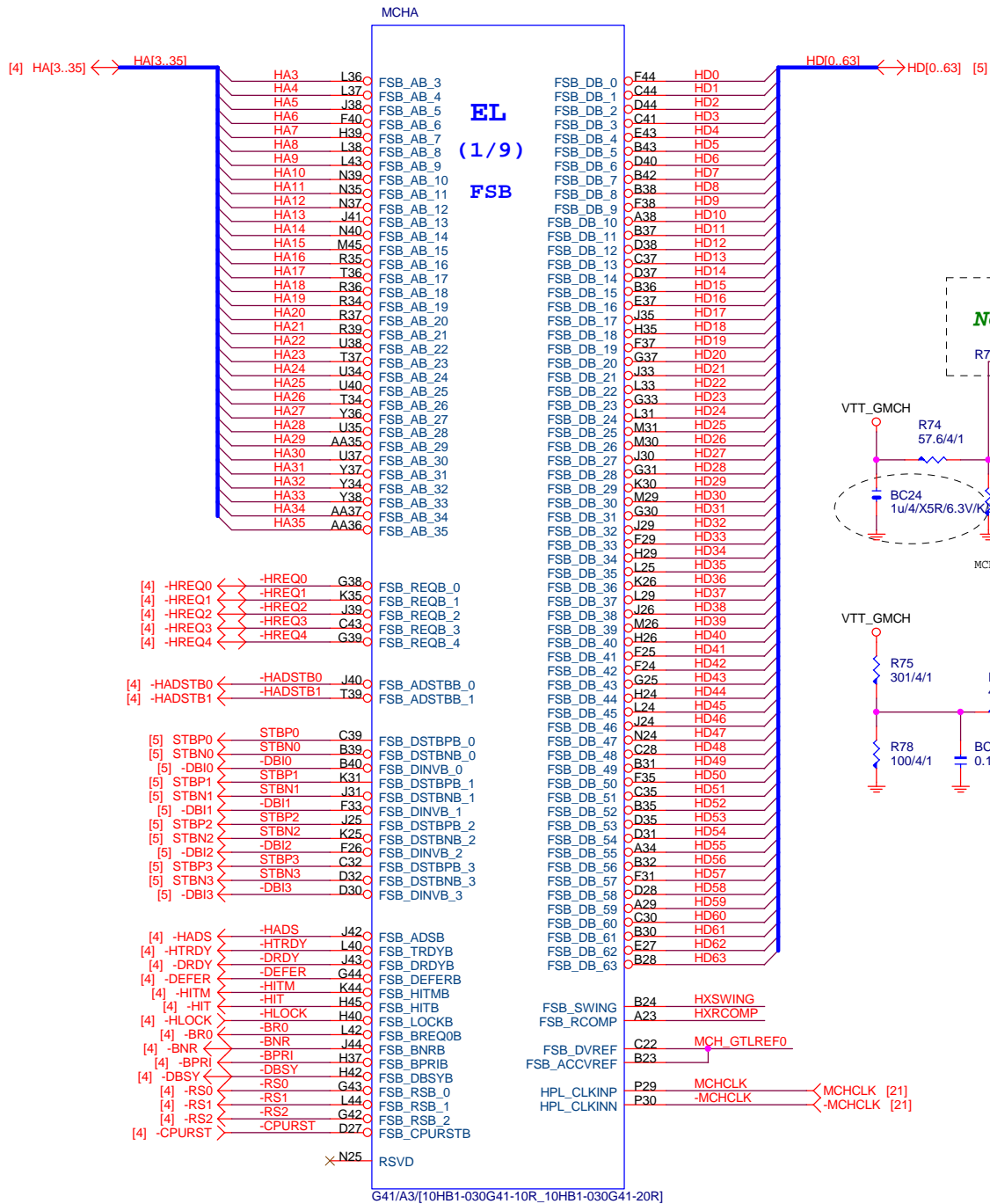


Gigabyte Technology

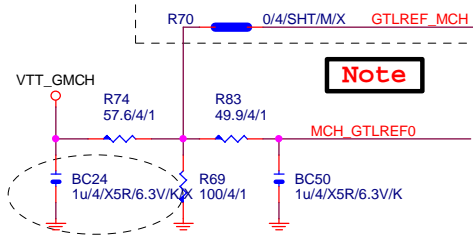
Title: **P4_LGA775-E,F,G,H**

Size B	Document Number	Rev
	GA-G41MT-S2PT	2.12

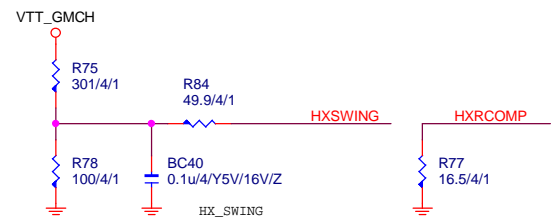
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Not used for CoreTM2 Duo and Wolfdale

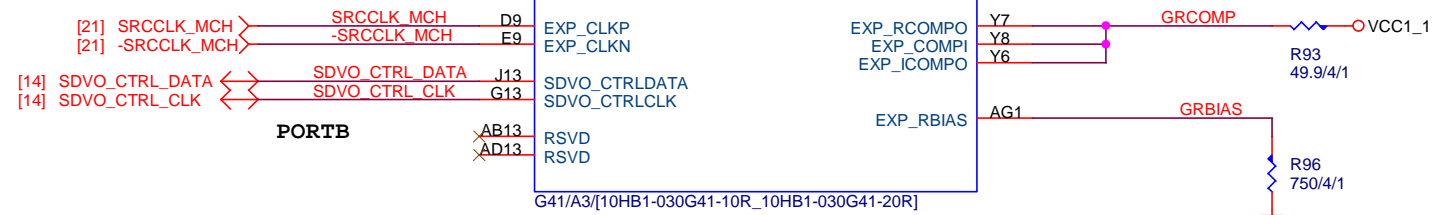
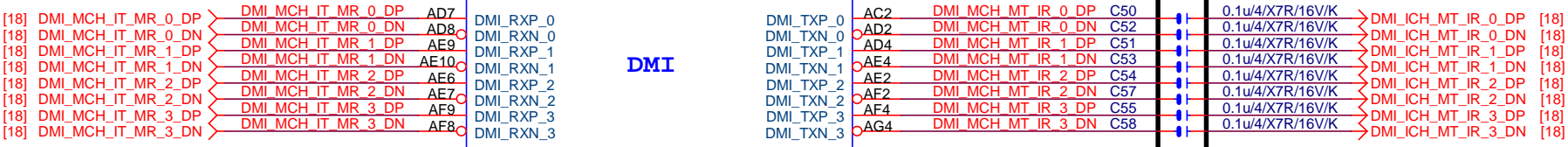
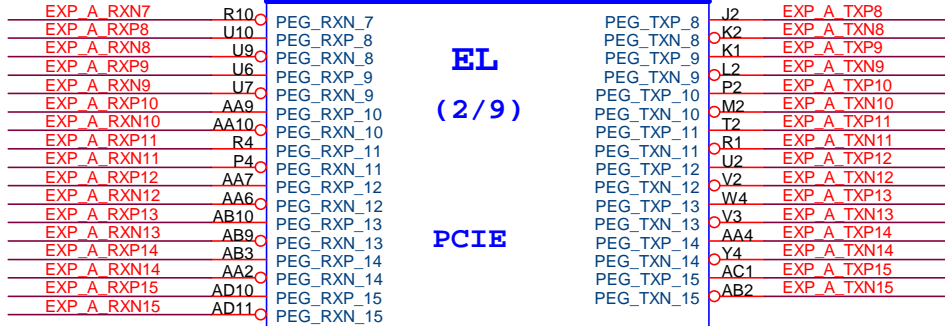
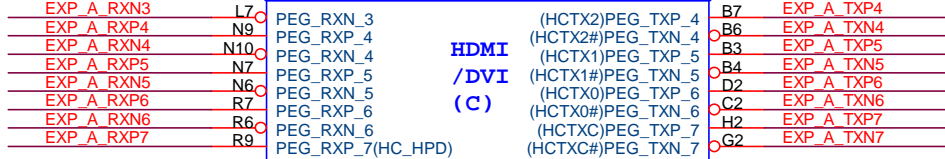
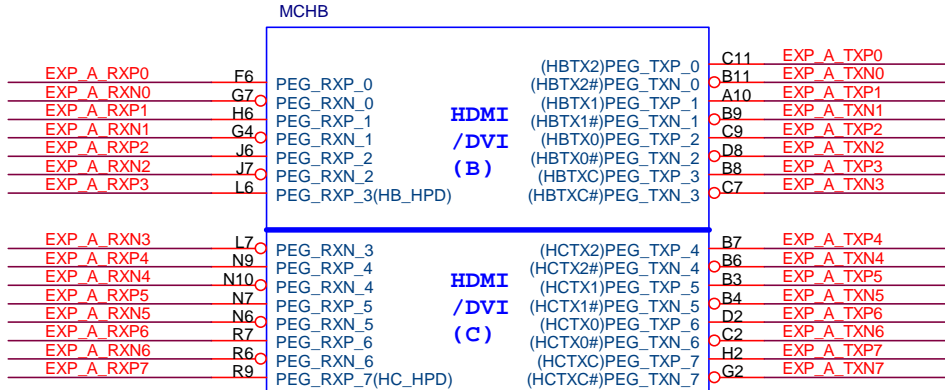


MCH_GTLREF VOLTAGE SHOULD BE 0.63 * VTT



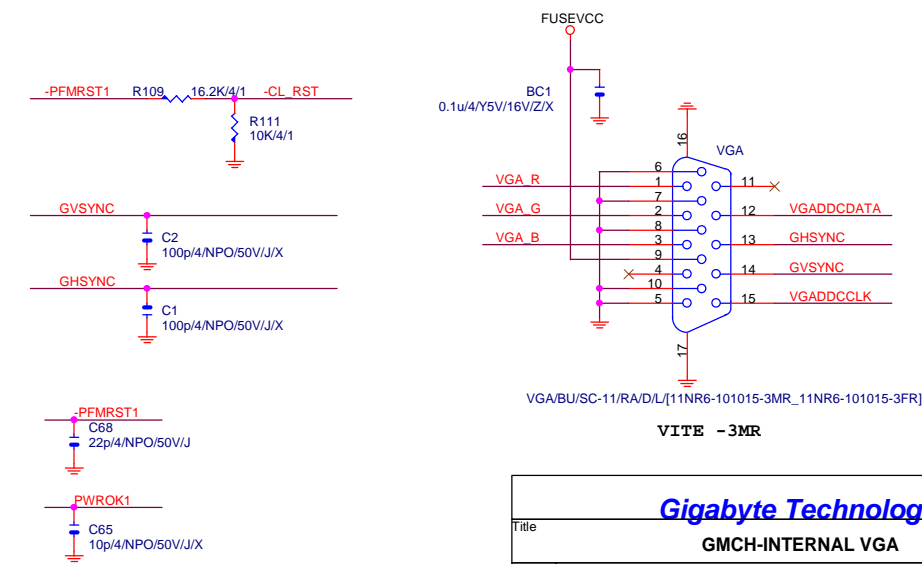
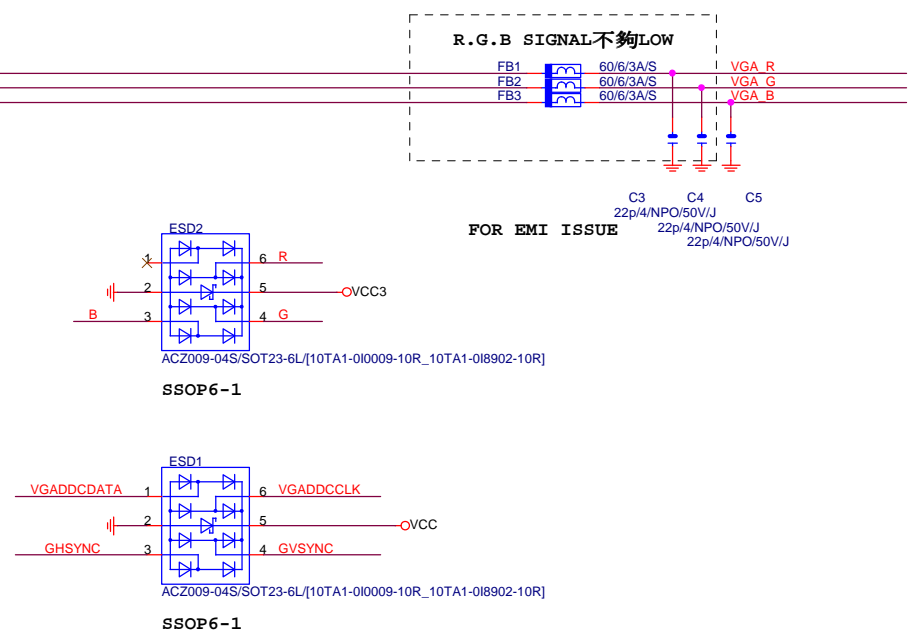
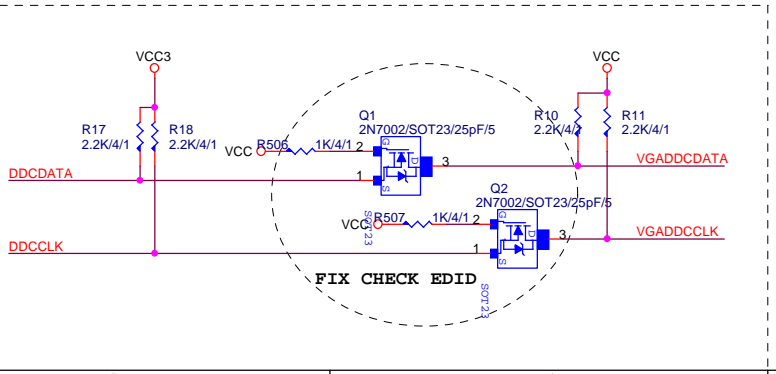
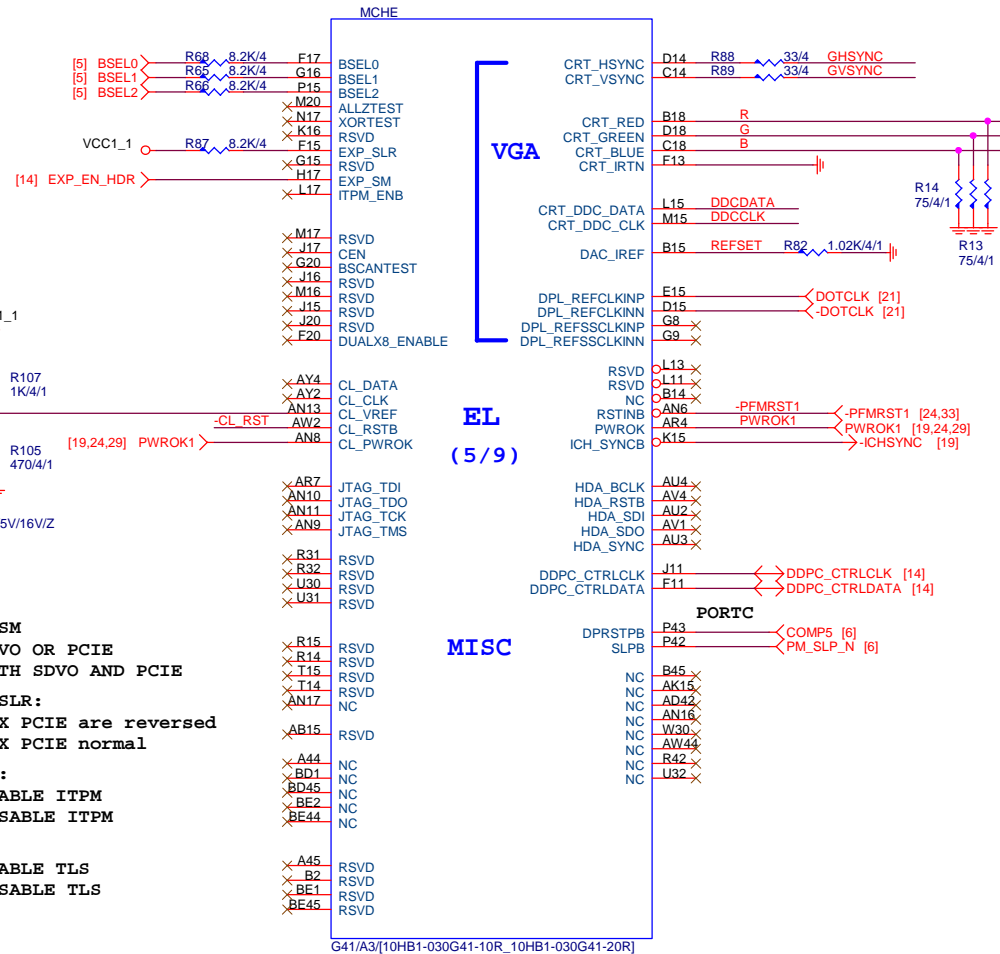
Gigabyte Technology		
GMCH-HOST		
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G41/A3/[10HB1-030G41-10R_10HB1-030G41-20R]



G41/A3/[10HB1-030G41-10R_10HB1-030G41-20R]

Gigabyte Technology		
GMCH-PCI E & DMI		
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GMCH-INTERNAL VGA		
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MCHH

A12	VSS	AK35
A15	VSS	AK38
A19	VSS	AK39
A27	VSS	AL44
A31	VSS	BD12
A36	VSS	BD17
A40	VSS	AN33
AA1	VSS	AN36
AA8	VSS	AN38
AA11	VSS	AN7
AA12	VSS	AP20
AA13	VSS	AP22
AA20	VSS	AP24
AA22	VSS	AP29
AA24	VSS	AP45
AA26	VSS	AR3
AA34	VSS	AR8
AA38	VSS	AR9
AA40	VSS	AR10
AA44	VSS	AR11
AB4	VSS	AR13
AB6	VSS	AR16
AB7	VSS	AR26
AB8	VSS	AR31
AB9	VSS	AR33
AB11	VSS	AR35
AB12	VSS	AR39
AB19	VSS	AT1
AB21	VSS	AT2
AB23	VSS	AT11
AB25	VSS	AT13
AB27	VSS	AT17
AB34	VSS	AT24
AB36	VSS	AT29
AB39	VSS	AY15
AC5	VSS	AT35
AC20	VSS	AU9
AC22	VSS	AU20
AC24	VSS	AU22
AC26	VSS	AU25
AC45	VSS	AU30
AD3	VSS	AV2
AD6	VSS	AV8
AD9	VSS	AV9
AD12	VSS	AV41
AD19	VSS	AV13
AD21	VSS	AV15
AD23	VSS	AV16
AD25	VSS	AV21
AD27	VSS	AV30
AD34	VSS	AV38
AD36	VSS	AW3
AD39	VSS	AW11
AE1	VSS	AW17
AE8	VSS	AW20
AE11	VSS	AW22
AE20	VSS	AW24
AE22	VSS	AW26
AE24	VSS	AW30
AE26	VSS	AY1
AE34	VSS	AY16
AE38	VSS	AY21
AE40	VSS	AY25
AE44	VSS	AY30
AF6	VSS	AY45
AF7	VSS	B21
AF10	VSS	B27
AF11	VSS	B29
AF12	VSS	B34
AF13	VSS	BA23
AF33	VSS	AV33
AF35	VSS	AB
AF39	VSS	B10
AG5	VSS	FR
AG19	VSS	AA16
AG21	VSS	AA17
AG23	VSS	AB16
AG25	VSS	AB17
AG27	VSS	AE12
AG45	VSS	AE13
AH2	VSS	AN21
AH3	VSS	AN22
AH4	VSS	AN24
AJ20	VSS	AN25
AJ22	VSS	AN26
AJ24	VSS	AP21
AJ26	VSS	AP25
AJ36	VSS	AU5
AJ39	VSS	AU6
AJ44	VSS	AU35
AJ45	VSS	AV6

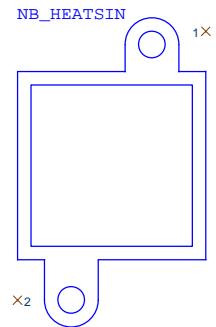
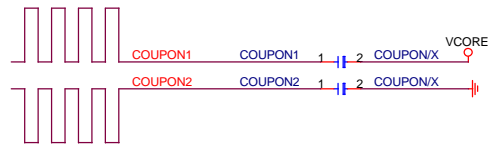
G41/A3/[10HB1-030G41-10R_10HB1-030G41-20R]

MCHI

BA5	VSS	L26
BB21	VSS	L30
BB25	VSS	L35
BB28	VSS	L39
BB6	VSS	L4
BD12	VSS	L6
BD17	VSS	L9
BD8	VSS	M1
BE10	VSS	M24
BE15	VSS	M25
BE19	VSS	M44
BE21	VSS	N11
BE24	VSS	N13
BE29	VSS	N16
BE34	VSS	N26
BE40	VSS	N29
C16	VSS	N30
CR9	VSS	N33
CR10	VSS	N36
C5	VSS	N38
D11	VSS	N8
D16	VSS	P16
D21	VSS	P17
D25	VSS	P25
D26	VSS	P26
D38	VSS	P31
D8	VSS	R11
D7	VSS	R12
E3	VSS	R16
E31	VSS	R17
E41	VSS	R19
E5	VSS	R2
F16	VSS	R20
F2	VSS	R30
F30	VSS	R38
F4	VSS	R45
F42	VSS	R5
F45	VSS	R8
G11	VSS	T10
G17	VSS	T11
G24	VSS	T12
G26	VSS	T13
G29	VSS	T16
G3	VSS	T17
G35	VSS	T19
H1	VSS	T20
H11	VSS	T3
H13	VSS	T30
H15	VSS	T31
H16	VSS	T32
H20	VSS	T33
H25	VSS	T35
H30	VSS	T38
H31	VSS	T4
H33	VSS	T40
H38	VSS	T6
H44	VSS	T7
H7	VSS	T8
H8	VSS	T9
H9	VSS	U1
J3	VSS	W2
J37	VSS	W20
J4	VSS	W22
J5	VSS	W24
J8	VSS	W26
J9	VSS	W44
K11	VSS	W45
K13	VSS	W5
K17	VSS	Y10
K20	VSS	Y11
K24	VSS	Y12
K29	VSS	Y13
K33	VSS	Y16
K45	VSS	Y17
L10	VSS	Y19
L16	VSS	Y2
L20	VSS	Y21
U11	VSS	Y23
U12	VSS	Y25
U13	VSS	Y27
U16	VSS	Y3
U17	VSS	Y35
U19	VSS	Y39
U20	VSS	Y9
U36	VSS	BD45
U39	VSS	BD2
U44	VSS	BD44
U8	VSS	BE3
W1	VSS	BE43
W16	VSS	C1
W17	VSS	C45
A3	VSS	F1
A43	VSS	BD43
A6	VSS	
B44	VSS	
BC1	VSS	
NC	AD30	
NC	AC30	
NC	AF30	
NC	AE30	

G41/A3/[10HB1-030G41-10R_10HB1-030G41-20R]

EL
(9/9)
GND

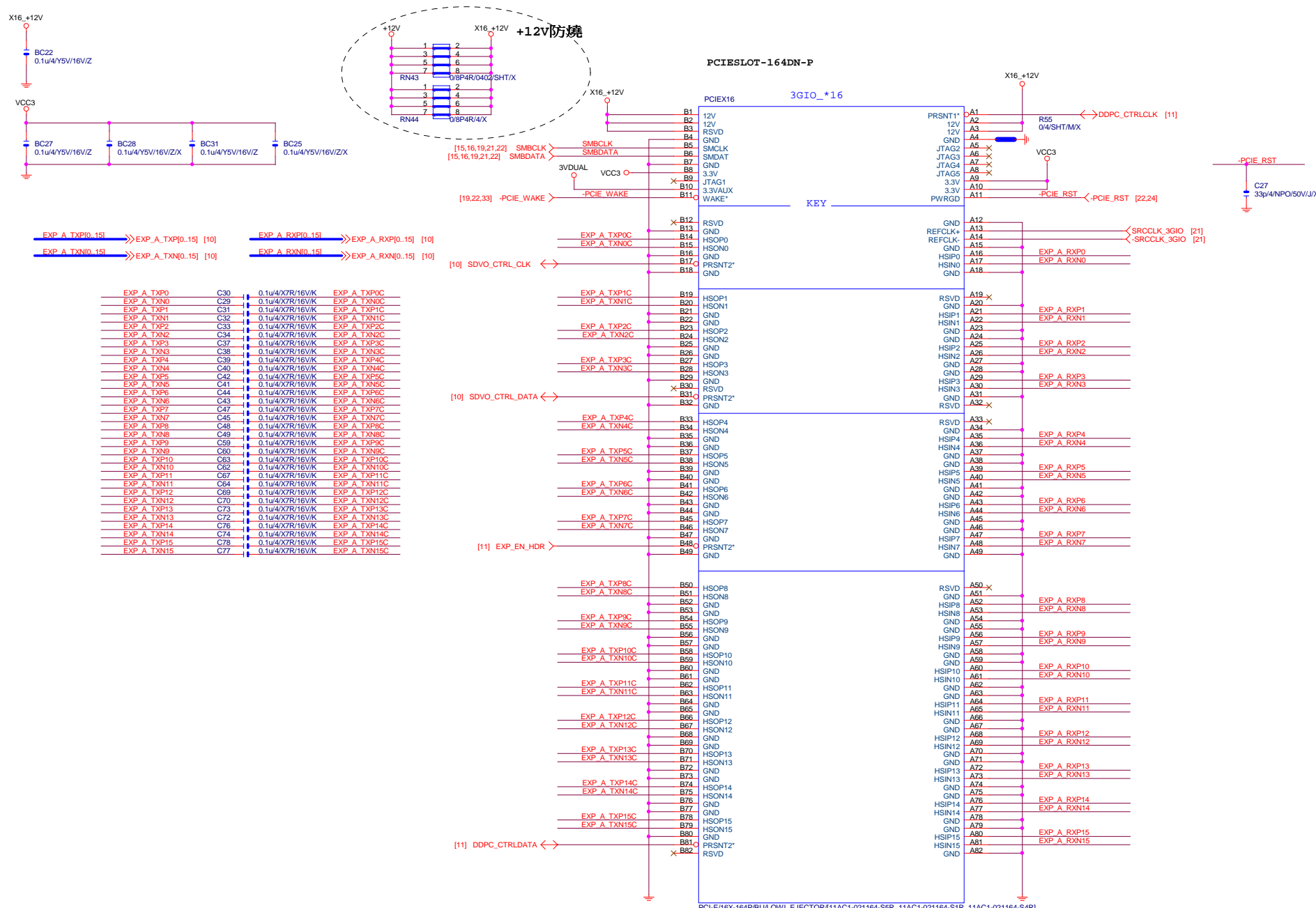


NB_HS
NB_HEATSINK[12SP2-S04520-03R_12SP2-S04520-01R_12SP2-S04520-02R]

BGASINK445A-L

NEW COST DOWN

Gigabyte Technology		
Title	GMCH-GND	
Size	Document Number	Rev
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[15,16,19,21,22] SMBCLK
[15,16,19,21,22] SMBDATA

[19,22,33] -PCIE_WAKE

[10] SDVO_CTRL_CLK

[10] SDVO_CTRL_DATA

[11] EXP_EN_HDR

[11] DDPC_CTRLDATA

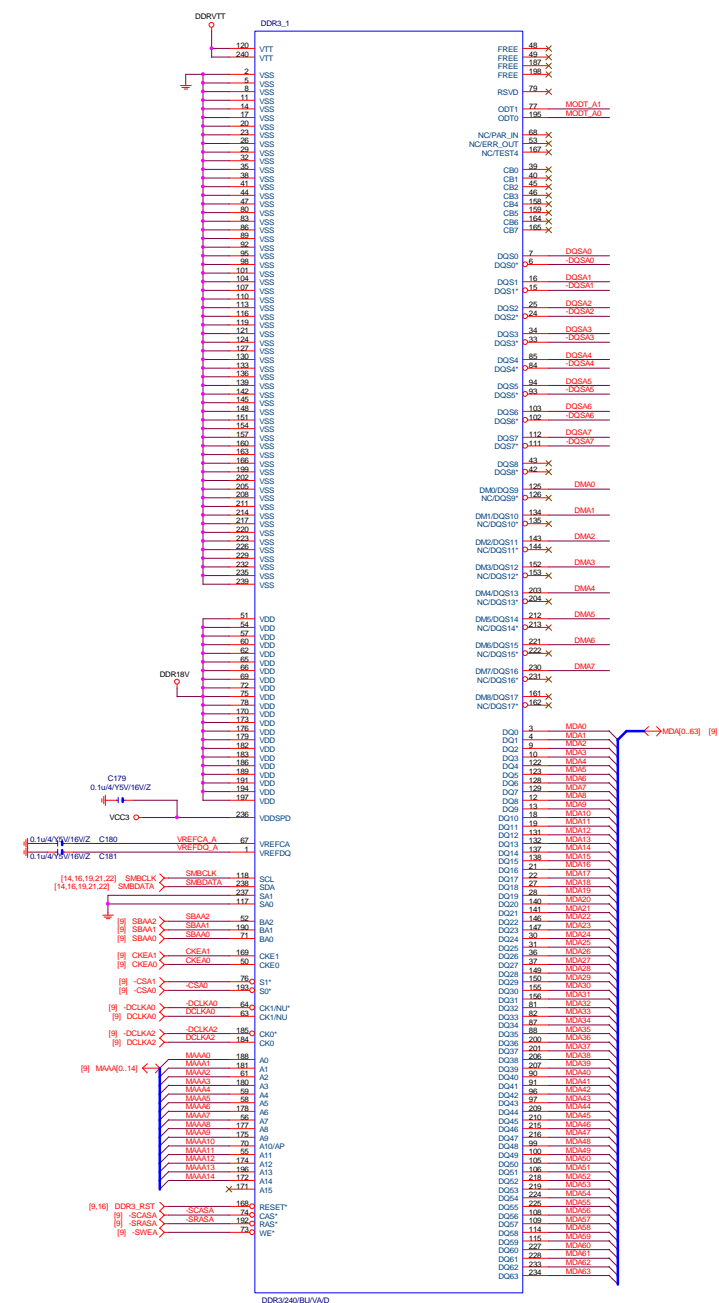
PCIESLOT-164DN-P

PCIEX16 3GIO_*16

PCI-E16x164P/BU/LOWL EJECTOR[11AC1-021164-S5R_11AC1-021164-S1R_11AC1-021164-S4R]

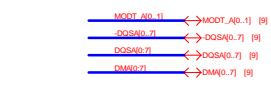
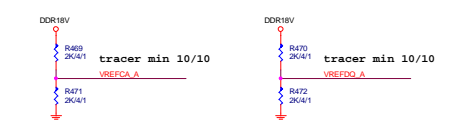
LOW LEFT BLUE

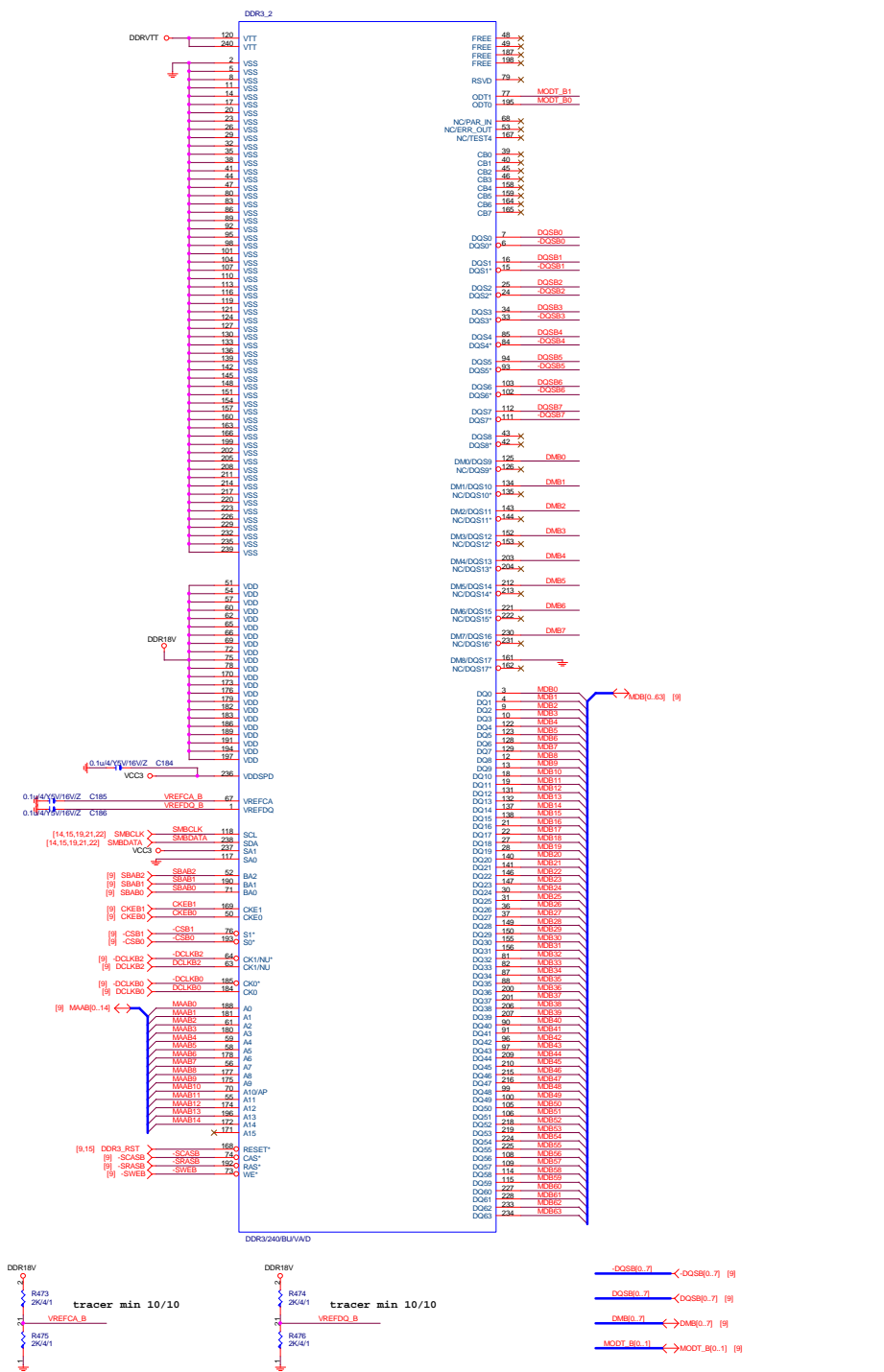
Gigabyte Technology		
PCI EXPRESS * 16		
Size	Document Number	Rev
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FREE	48	X
FREE	49	X
FREE	187	X
FREE	188	X
FREE	79	X
RSVD	77	X
ODT1	77	MODT_A1
ODT0	156	MODT_A0
NCIPAR_IN	68	X
NCERR_OUT	53	X
NCTEST4	160	X
C80	39	X
C81	40	X
C82	45	X
C83	46	X
C84	156	X
C85	157	X
C86	158	X
C87	159	X
DOS0	7	DOSA0
DOS1	16	DOSA1
DOS2	25	DOSA2
DOS3	34	DOSA3
DOS4	43	DOSA4
DOS5	52	DOSA5
DOS6	61	DOSA6
DOS7	70	DOSA7
DM0	128	DMA0
DM1	134	DMA1
DM2	140	DMA2
DM3	146	DMA3
DM4	152	DMA4
DM5	158	DMA5
DM6	164	DMA6
DM7	170	DMA7
MD0	3	MDA0
MD1	4	MDA1
MD2	5	MDA2
MD3	6	MDA3
MD4	7	MDA4
MD5	8	MDA5
MD6	9	MDA6
MD7	10	MDA7
MDAT0	11	MDA0
MDAT1	12	MDA1
MDAT2	13	MDA2
MDAT3	14	MDA3
MDAT4	15	MDA4
MDAT5	16	MDA5
MDAT6	17	MDA6
MDAT7	18	MDA7
SBA0	19	BA0
SBA1	20	BA1
SBA2	21	BA2
SBA3	22	BA3
SBA4	23	BA4
SBA5	24	BA5
SBA6	25	BA6
SBA7	26	BA7
CKEA0	27	CKE0
CKEA1	28	CKE1
CKEA2	29	CKE2
CKEA3	30	CKE3
CKEA4	31	CKE4
CKEA5	32	CKE5
CKEA6	33	CKE6
CKEA7	34	CKE7
CSA0	35	CS0
CSA1	36	CS1
CSA2	37	CS2
CSA3	38	CS3
CSA4	39	CS4
CSA5	40	CS5
CSA6	41	CS6
CSA7	42	CS7
DCLKA0	43	CK1IN0
DCLKA1	44	CK1IN1
DCLKA2	45	CK1IN2
DCLKA3	46	CK1IN3
DCLKA4	47	CK1IN4
DCLKA5	48	CK1IN5
DCLKA6	49	CK1IN6
DCLKA7	50	CK1IN7
MAA0	51	MA0
MAA1	52	MA1
MAA2	53	MA2
MAA3	54	MA3
MAA4	55	MA4
MAA5	56	MA5
MAA6	57	MA6
MAA7	58	MA7
SCSA	59	RAS0
SRASA	60	RAS1
SVEA	61	WE0

MDA0.03 [9]





FREE	48	X
FREE	49	X
FREE	102	X
FREE	198	X
RSVD	79	X
ODT1	77	MODT_B1
ODT0	156	MODT_B0
NC/PAR_IN	68	X
NC/ERR_OUT	33	X
NC/TEST4	167	X
CB0	39	X
CB1	40	X
CB2	45	X
CB3	158	X
CB4	159	X
CB5	164	X
CB6	165	X
CB7	166	X
DQS0	7	DQS0
DQS0'	6	DQS0
DQS1	16	DQS1
DQS1'	15	DQS1
DQS2	25	DQS2
DQS2'	24	DQS2
DQS3	34	DQS3
DQS3'	33	DQS3
DQS4	85	DQS4
DQS4'	84	DQS4
DQS5	94	DQS5
DQS5'	93	DQS5
DQS6	100	DQS6
DQS6'	100	DQS6
DQS7	112	DQS7
DQS7'	111	DQS7
DQS8	43	X
DQS8'	42	X
DM/DQS9	126	DMB0
NC/DQS9'	126	X
DM/DQS10	138	DMB1
NC/DQS10'	138	X
DM/DQS11	143	DMB2
NC/DQS11'	144	X
DM/DQS12	150	DMB3
NC/DQS12'	153	X
DM/DQS13	200	DMB4
NC/DQS13'	204	X
DM/DQS14	212	DMB5
NC/DQS14'	213	X
DM/DQS15	221	DMB6
NC/DQS15'	224	X
DM/DQS16	230	DMB7
NC/DQS16'	231	X
DM/DQS17	161	X
NC/DQS17'	162	X
DQ0	3	MBB0
DQ1	4	MBB1
DQ2	9	MBB2
DQ3	10	MBB3
DQ4	122	MBB4
DQ5	123	MBB5
DQ6	128	MBB6
DQ7	129	MBB7
DQ8	12	MBB8
DQ9	13	MBB9
DQ10	18	MBB10
DQ11	18	MBB11
DQ12	131	MBB12
DQ13	132	MBB13
DQ14	137	MBB14
DQ15	138	MBB15
DQ16	21	MBB16
DQ17	22	MBB17
DQ18	27	MBB18
DQ19	28	MBB19
DQ20	140	MBB20
DQ21	141	MBB21
DQ22	146	MBB22
DQ23	147	MBB23
DQ24	36	MBB24
DQ25	31	MBB25
DQ26	37	MBB26
DQ27	148	MBB27
DQ28	150	MBB28
DQ29	158	MBB29
DQ30	156	MBB30
DQ31	156	MBB31
DQ32	81	MBB32
DQ33	82	MBB33
DQ34	87	MBB34
DQ35	88	MBB35
DQ36	200	MBB36
DQ37	201	MBB37
DQ38	206	MBB38
DQ39	207	MBB39
DQ40	90	MBB40
DQ41	91	MBB41
DQ42	96	MBB42
DQ43	97	MBB43
DQ44	205	MBB44
DQ45	210	MBB45
DQ46	215	MBB46
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DQ51	106	MBB51
DQ52	218	MBB52
DQ53	219	MBB53
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DQ56	108	MBB56
DQ57	108	MBB57
DQ58	114	MBB58
DQ59	115	MBB59
DQ60	227	MBB60
DQ61	228	MBB61
DQ62	233	MBB62
DQ63	234	MBB63

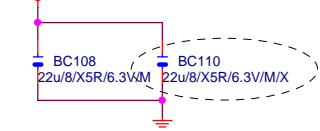
- ← DQS0_0.7 [8]
- ← DQS80_0.7 [8]
- ← DMB0_0.7 [8]
- ← MODT_B0_0.1 [8]

DDR TERMINATION CHANNEL A

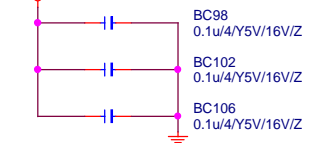
DDR18V Decouple



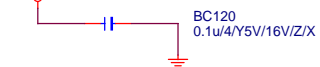
DDRVTT Decouple



DDR18V Decouple

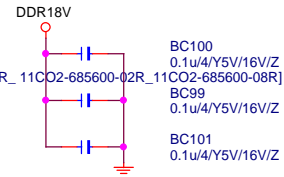


DDRVTT Decouple

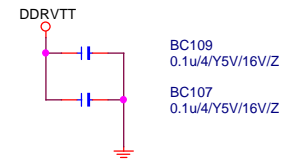


DDR TERMINATION CHANNEL B

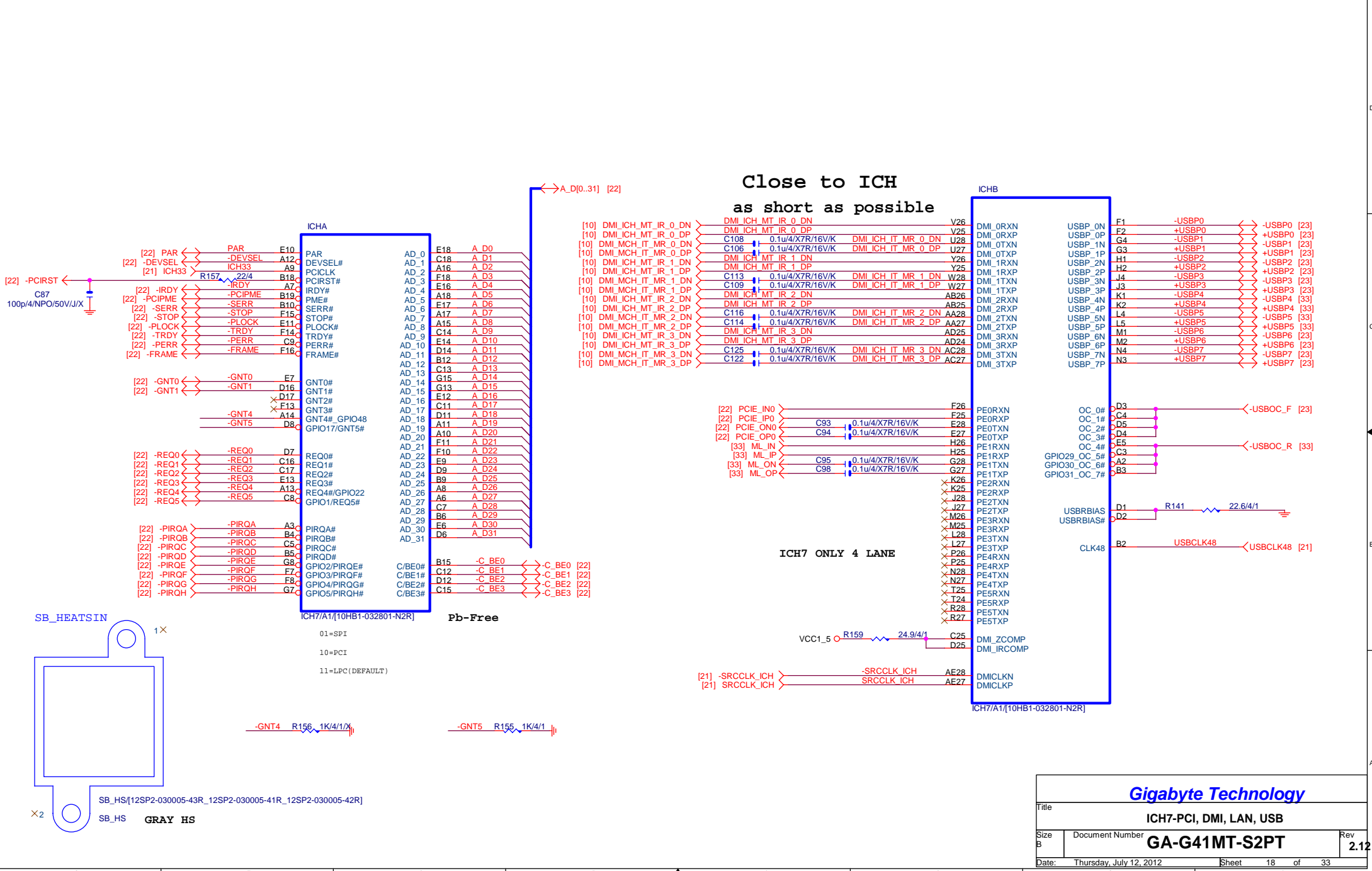
DDR18V Decouple



DDRVTT Decouple



Gigabyte Technology		
Title DDRII TERMINATOR		
Size Custom	Document Number GA-G41MT-S2PT	Rev 2.12
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Close to ICH
as short as possible

ICHB

ICH7 ONLY 4 LANE

SB_HEATSIN 1X

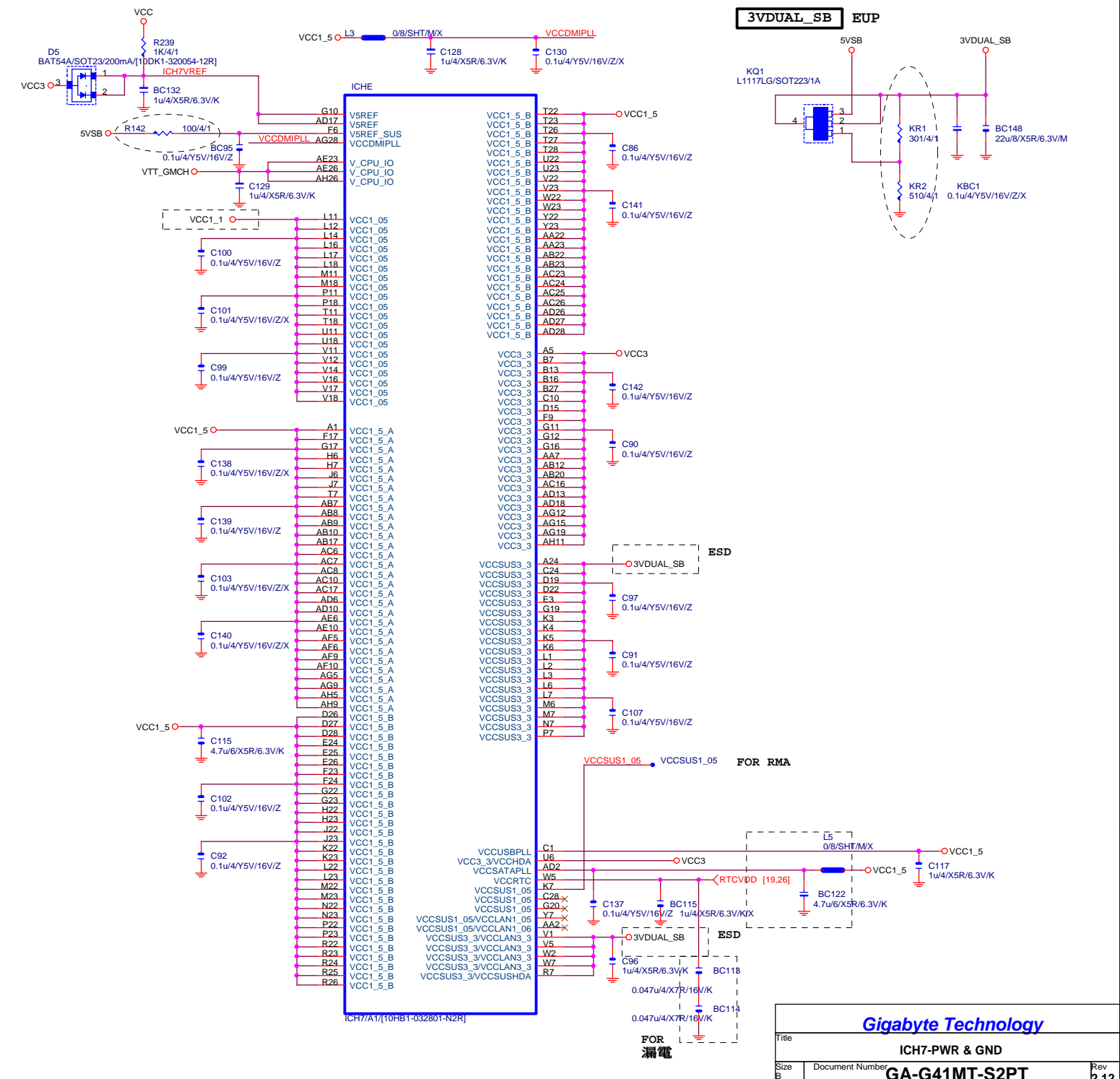
SB_HS(12SP2-030005-43R_12SP2-030005-41R_12SP2-030005-42R)
SB_HS GRAY HS X2

Gigabyte Technology

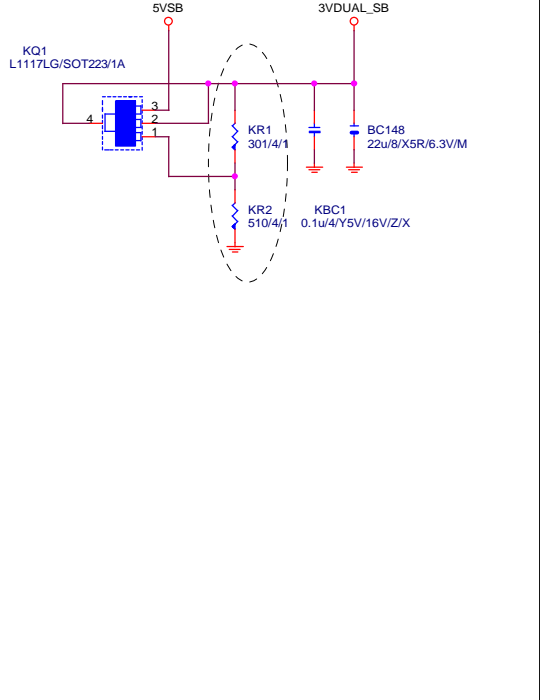
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Size	Document Number	GA-G41MT-S2PT	
Date:	Thursday, July 12, 2012	Sheet	18 of 33
Rev	2.12		

ICHF		
A4	VSS1	VSS101
A23	VSS2	VSS102
B1	VSS3	VSS103
B8	VSS4	VSS104
B11	VSS5	VSS105
B14	VSS6	VSS106
B17	VSS7	VSS107
B20	VSS8	VSS108
B26	VSS9	VSS109
B28	VSS10	VSS110
C2	VSS11	VSS111
C6	VSS12	VSS112
D10	VSS13	VSS113
D13	VSS14	VSS114
D18	VSS15	VSS115
D21	VSS16	VSS116
D24	VSS17	VSS117
E1	VSS18	VSS118
E2	VSS19	VSS119
E8	VSS20	VSS120
E15	VSS21	VSS121
F3	VSS22	VSS122
F4	VSS23	VSS123
F12	VSS24	VSS124
F27	VSS25	VSS125
F28	VSS26	VSS126
G1	VSS27	VSS127
G1	VSS28	VSS128
G2	VSS29	VSS129
G6	VSS30	VSS130
G9	VSS31	VSS131
G14	VSS32	VSS132
G18	VSS33	VSS133
G21	VSS34	VSS134
G24	VSS35	VSS135
G25	VSS36	VSS136
G26	VSS37	VSS137
H3	VSS38	VSS138
H4	VSS39	VSS139
H5	VSS40	VSS140
H24	VSS41	VSS141
H27	VSS42	VSS142
H28	VSS43	VSS143
J1	VSS44	VSS144
J2	VSS45	VSS145
J5	VSS46	VSS146
J24	VSS47	VSS147
J25	VSS48	VSS148
J26	VSS49	VSS149
K24	VSS50	VSS150
K27	VSS51	VSS151
K28	VSS52	VSS152
L13	VSS53	VSS153
L15	VSS54	VSS154
L24	VSS55	VSS155
L25	VSS56	VSS156
L26	VSS57	VSS157
M3	VSS58	VSS158
M4	VSS59	VSS159
M5	VSS60	VSS160
M12	VSS61	VSS161
M13	VSS62	VSS162
M14	VSS63	VSS163
M15	VSS64	VSS164
M16	VSS65	VSS165
M17	VSS66	VSS166
M24	VSS67	VSS167
M27	VSS68	VSS168
M28	VSS69	VSS169
N1	VSS70	VSS170
N1	VSS71	VSS171
N5	VSS72	VSS172
N6	VSS73	VSS173
N11	VSS74	VSS174
N12	VSS75	VSS175
N13	VSS76	VSS176
N14	VSS77	VSS177
N15	VSS78	VSS178
N16	VSS79	VSS179
N17	VSS80	VSS180
N18	VSS81	VSS181
N24	VSS82	VSS182
N25	VSS83	VSS183
N26	VSS84	VSS184
P3	VSS85	VSS185
P4	VSS86	VSS186
P12	VSS87	VSS187
P13	VSS88	VSS188
P14	VSS89	VSS189
P15	VSS90	VSS190
P16	VSS91	VSS191
P17	VSS92	VSS192
P24	VSS93	VSS193
P27	VSS94	VSS194
P28	VSS95	VSS195
R1	VSS96	VSS196
R11	VSS97	VSS197
R12	VSS98	VSS198
R13	VSS99	VSS199
	VSS100	VSS200

ICHE		
R14	VSS101	VSS101
R15	VSS102	VSS102
R16	VSS103	VSS103
R17	VSS104	VSS104
R18	VSS105	VSS105
T6	VSS106	VSS106
T12	VSS107	VSS107
T13	VSS108	VSS108
T14	VSS109	VSS109
T15	VSS110	VSS110
T16	VSS111	VSS111
T17	VSS112	VSS112
U4	VSS113	VSS113
U12	VSS114	VSS114
U13	VSS115	VSS115
U14	VSS116	VSS116
U15	VSS117	VSS117
U16	VSS118	VSS118
U17	VSS119	VSS119
U24	VSS120	VSS120
U25	VSS121	VSS121
U26	VSS122	VSS122
V2	VSS123	VSS123
V13	VSS124	VSS124
V15	VSS125	VSS125
V24	VSS126	VSS126
V27	VSS127	VSS127
V28	VSS128	VSS128
W6	VSS129	VSS129
W24	VSS130	VSS130
W25	VSS131	VSS131
W26	VSS132	VSS132
Y3	VSS133	VSS133
Y24	VSS134	VSS134
Y27	VSS135	VSS135
Y28	VSS136	VSS136
AA1	VSS137	VSS137
AA24	VSS138	VSS138
AA25	VSS139	VSS139
AA26	VSS140	VSS140
AB4	VSS141	VSS141
AB6	VSS142	VSS142
AB11	VSS143	VSS143
AB14	VSS144	VSS144
AB16	VSS145	VSS145
AB19	VSS146	VSS146
AB21	VSS147	VSS147
AB24	VSS148	VSS148
AB27	VSS149	VSS149
AB28	VSS150	VSS150
AC2	VSS151	VSS151
AC5	VSS152	VSS152
AC9	VSS153	VSS153
AC11	VSS154	VSS154
AD1	VSS155	VSS155
AD3	VSS156	VSS156
AD4	VSS157	VSS157
AD7	VSS158	VSS158
AD8	VSS159	VSS159
AD11	VSS160	VSS160
AD15	VSS161	VSS161
AD19	VSS162	VSS162
AD23	VSS163	VSS163
AE2	VSS164	VSS164
AE4	VSS165	VSS165
AE8	VSS166	VSS166
AE11	VSS167	VSS167
AE13	VSS168	VSS168
AE18	VSS169	VSS169
AE21	VSS170	VSS170
AE24	VSS171	VSS171
AE25	VSS172	VSS172
AF2	VSS173	VSS173
AF4	VSS174	VSS174
AF8	VSS175	VSS175
AF11	VSS176	VSS176
AF27	VSS177	VSS177
AF28	VSS178	VSS178
AG1	VSS179	VSS179
AG3	VSS180	VSS180
AG7	VSS181	VSS181
AG14	VSS182	VSS182
AG17	VSS183	VSS183
AG20	VSS184	VSS184
AG25	VSS185	VSS185
AH1	VSS186	VSS186
AH3	VSS187	VSS187
AH7	VSS188	VSS188
AH12	VSS189	VSS189
AH23	VSS190	VSS190
AH27	VSS191	VSS191
C27	VSS192	VSS192
E4	VSS193	VSS193
AG11	VSS194	VSS194



3VDUAL_SB EUP



ESD

FOR RMA

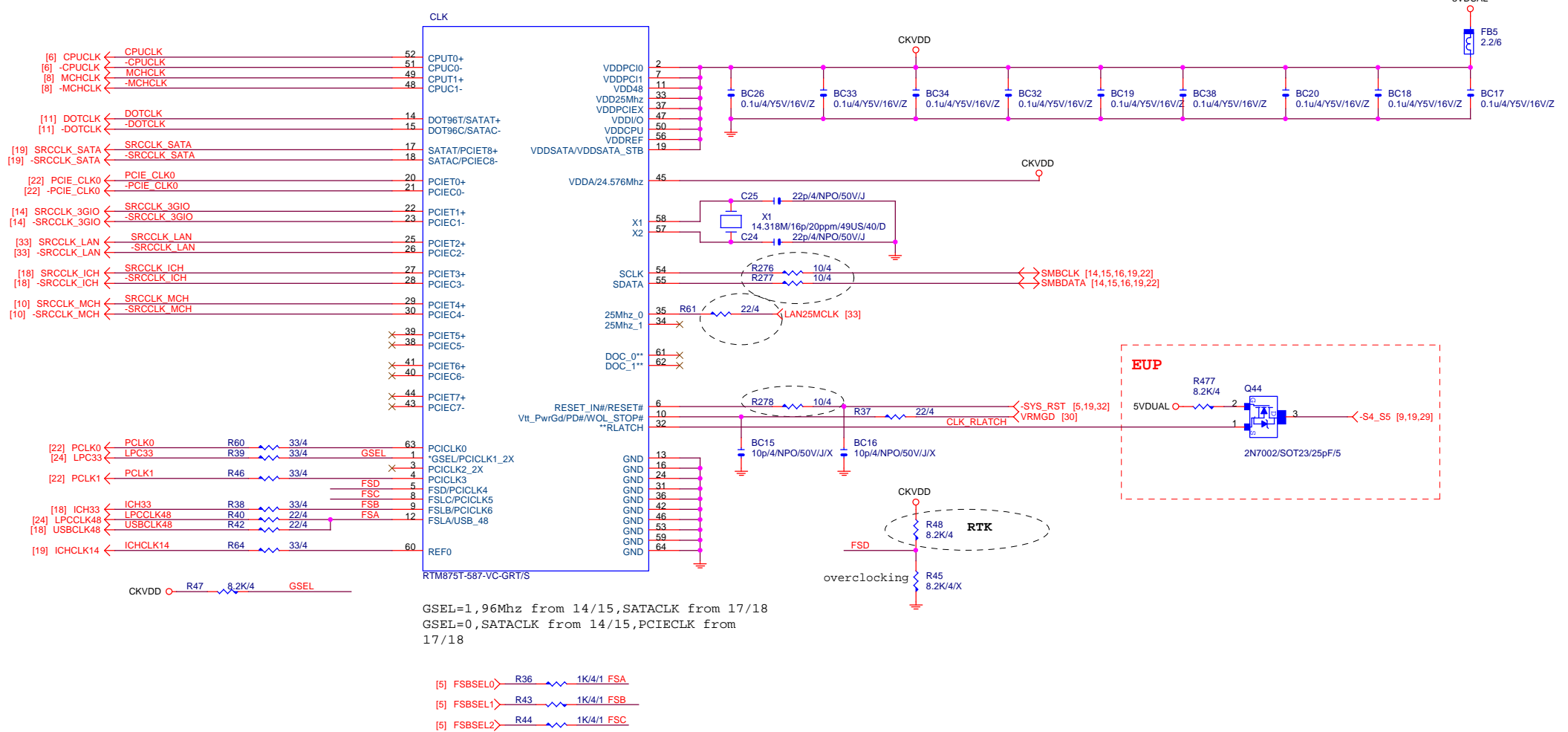
FOR 漏電

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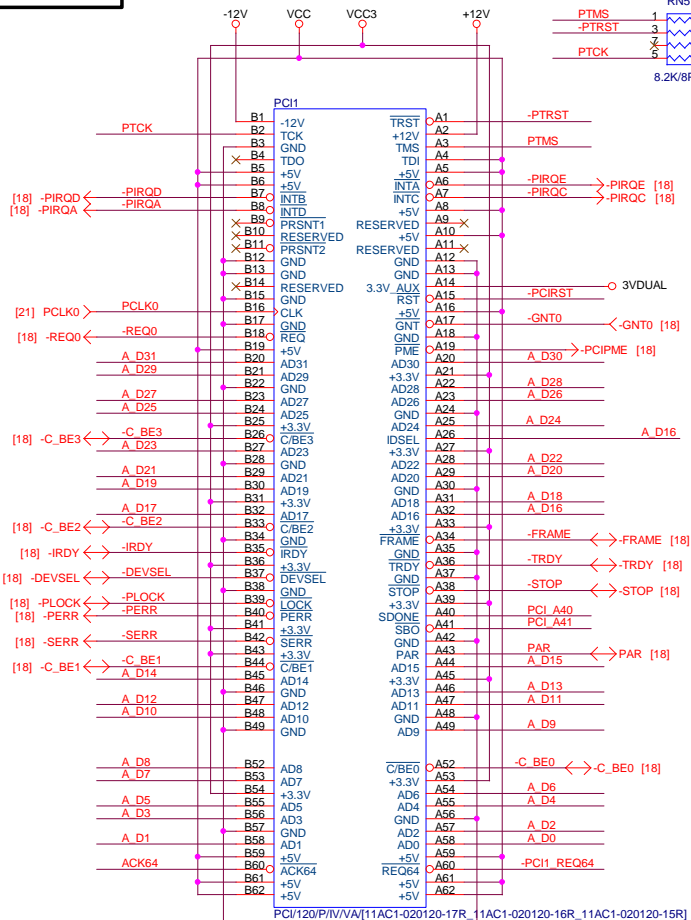
ICH7-PWR & GND

Size B Document Number **GA-G41MT-S2PT** Rev **2.12**

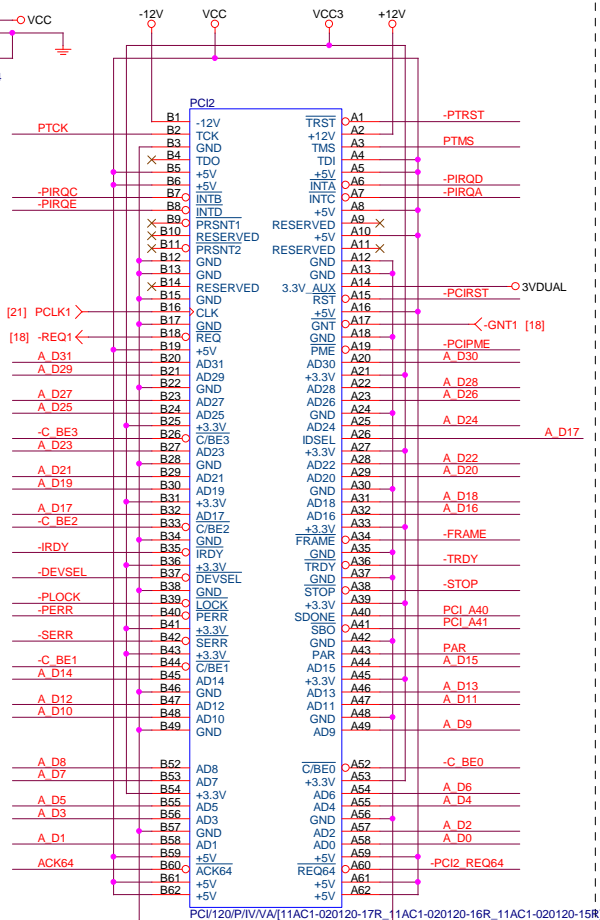
Date: Thursday, July 12, 2012 Sheet 20 of 33



PCI1, 2 SLOT

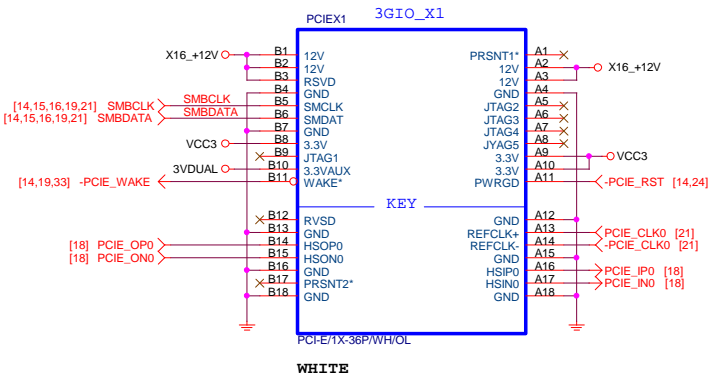


AD_16 / -PIRQ (E-D-C-A) / -REQ0 / -GNT0



AD_17 / -PIRQ (D-C-A-E) / -REQ1 / -GNT1

PCIE*1



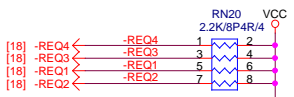
[18] A_D[0..31] ↔ A_D[0..31]

-PCIRST ↔ PCIRST [18]

C28 33p/4/NPO/50V/JJX

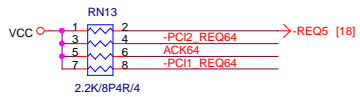
Place close to PCII

[14,15,16,19,21] SMBCLK ↔ PCI_A40
[14,15,16,19,21] SMBDATA ↔ PCI_A41

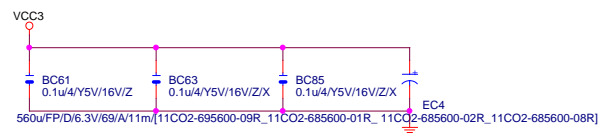
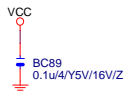
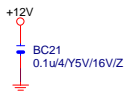
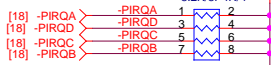
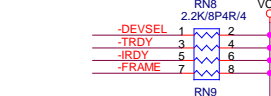


[18] -REQ0 ↔ -REQ0

R652 2.2K/4/1



VCC ↔ -PCII_REQ64 → REQ5 [18]

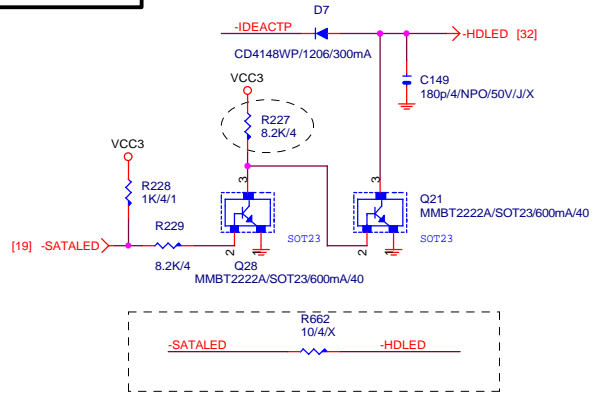


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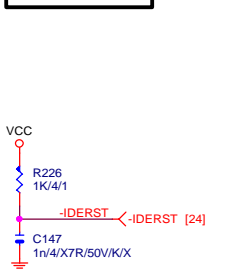
PCI SLOT 1, 2/PCIE1
GA-G41MT-S2PT

Title	PCI SLOT 1, 2/PCIE1		Rev	2.12
Size	Document Number	GA-G41MT-S2PT		
Custort				
Date:	Thursday, July 12, 2012	Sheet	22	of 33

IDE/SATA LED



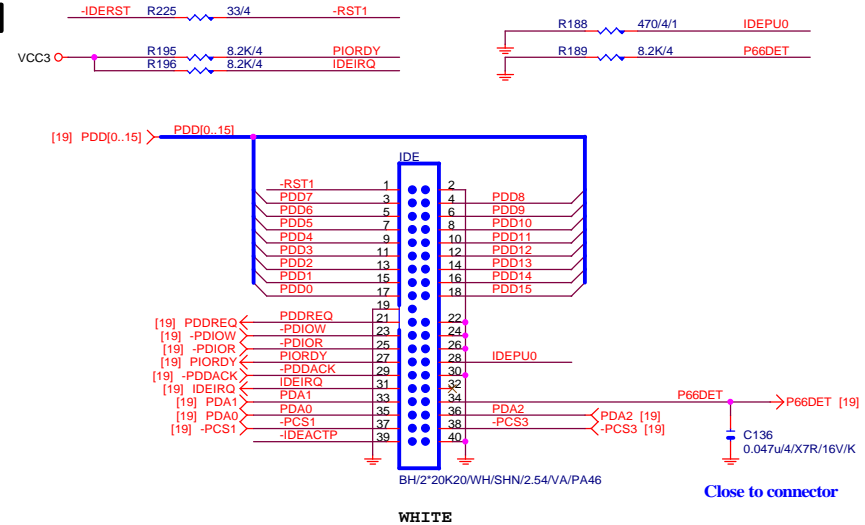
IDE RESET



FLOPPY

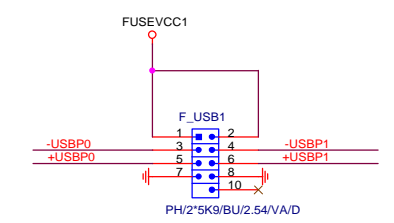
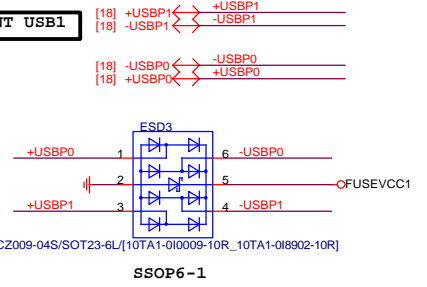
N/A

IDE

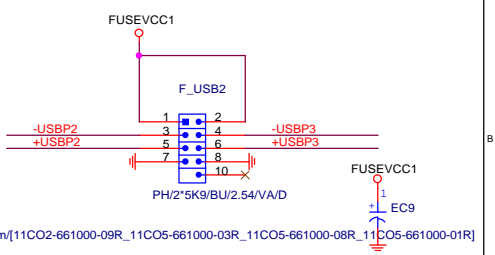
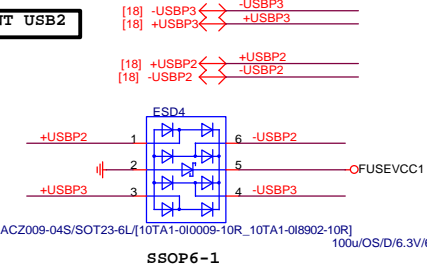


WHITE

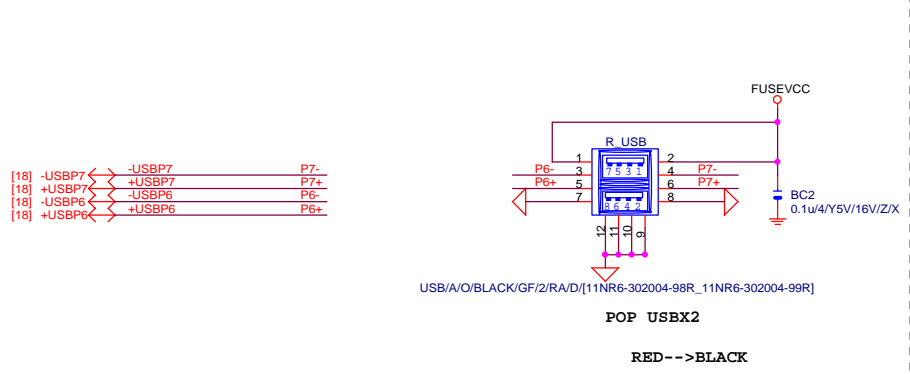
FRONT USB1



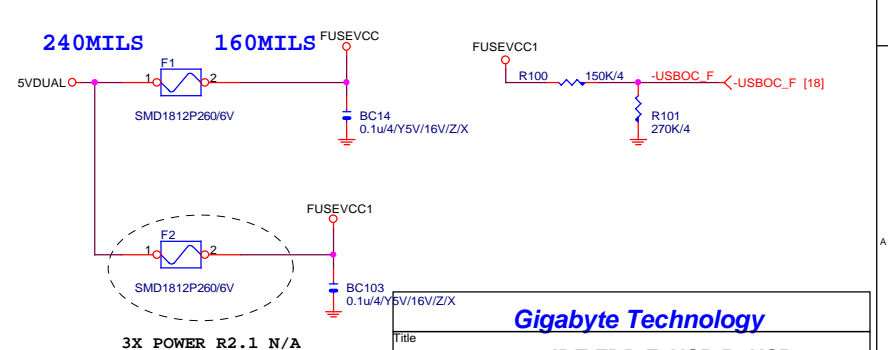
FRONT USB2



REAR USB

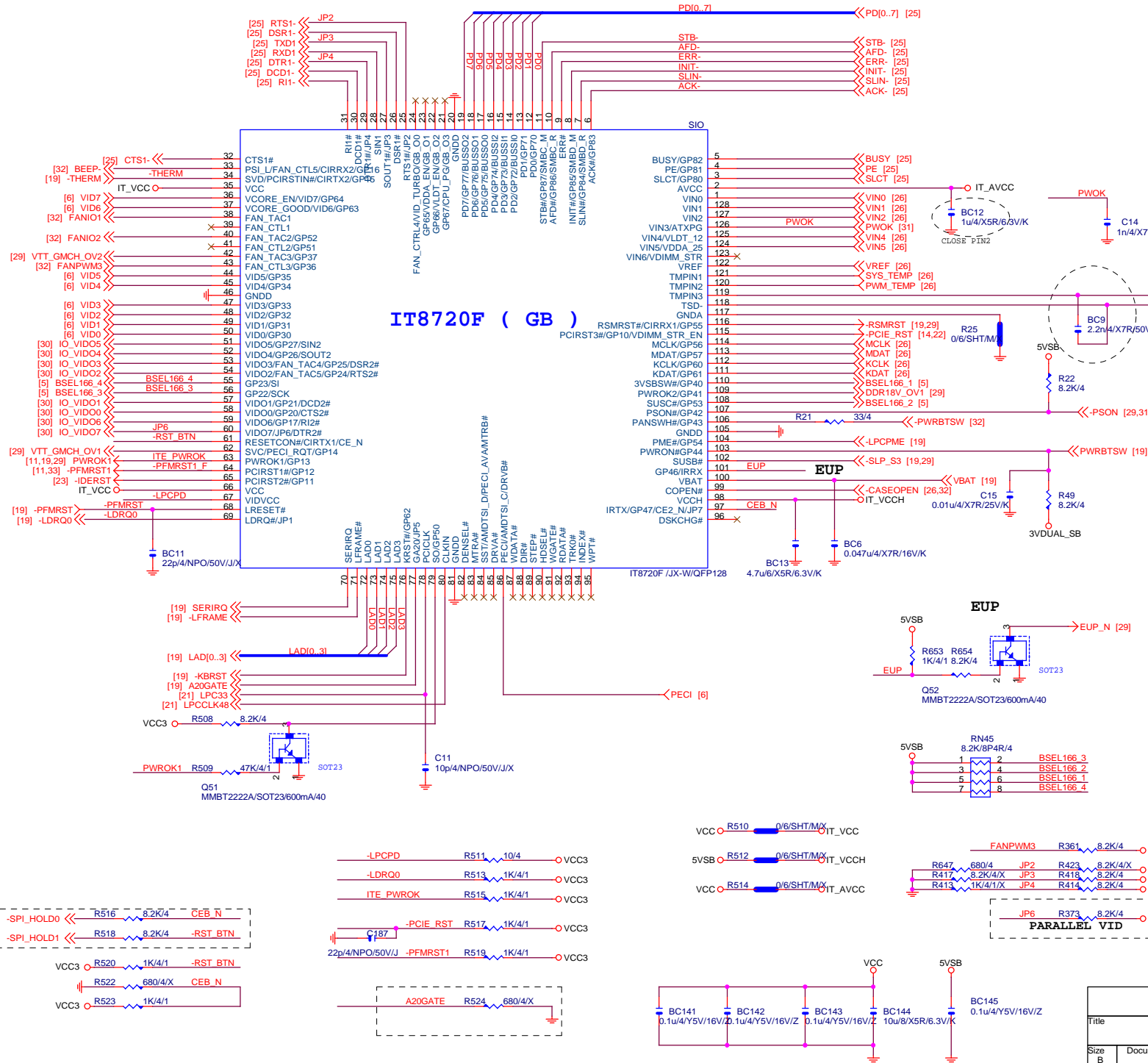


USB FUSE



Gigabyte Technology

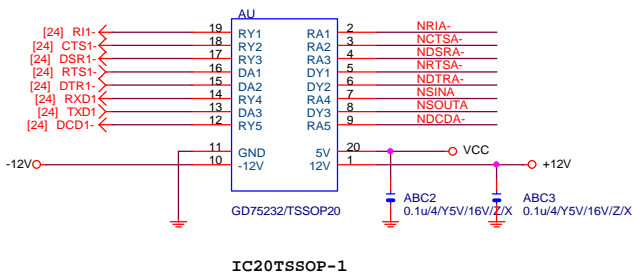
Title		
IDE,FDD,F_USB,R_USB		
Size	Document Number	Rev
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Date:	Thursday, July 12, 2012	Sheet 23 of 33



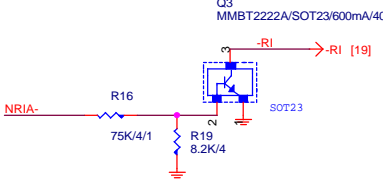
ITE8720 Power on Strapping

JP2	1	Disable VID/SVID output pins
	0	Enable VIDO0~7 output pins
JP3	1	SPI-Flash Disable
	0	SPI-Flash Enable
JP4	1	k8 power sequency function is Disable
	0	k8 power sequency function is Enable
JP5	1	Disable WDT reset PWROK
	0	Enable WDT reset PWROK
JP6	1	Parallel VID output
	0	Serial VID output
JP7	1	Enable Dual BIOS Function
	0	Disable Dual BIOS Function

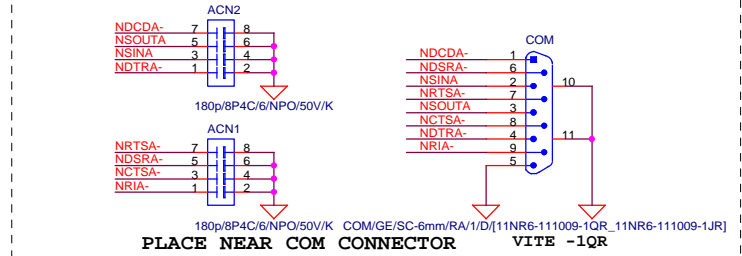
COM



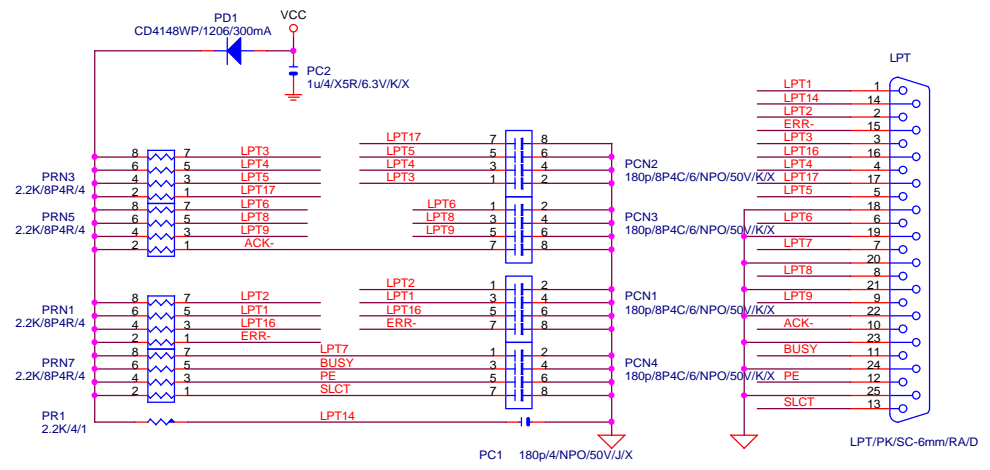
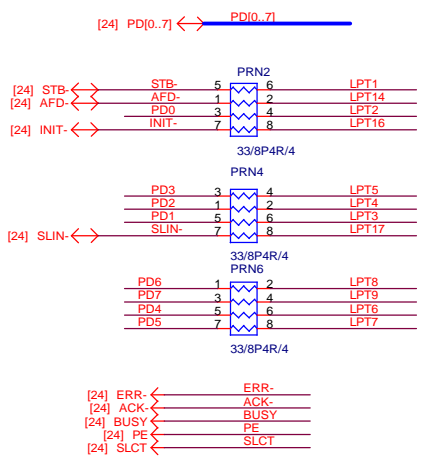
RING IN



EXTERNAL COMA

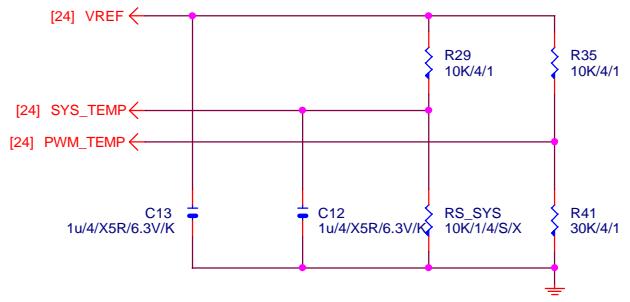


LPT PORT

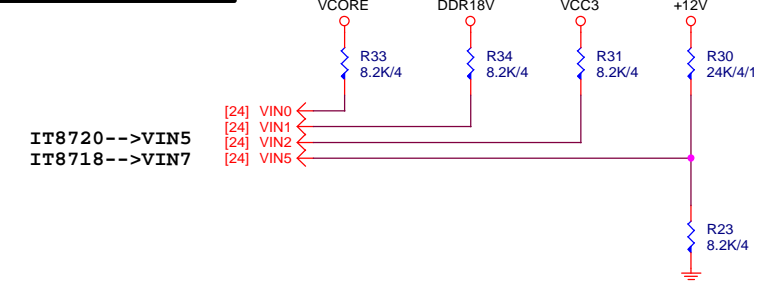


Gigabyte Technology	
COM & LPT PORT	
Title	COM & LPT PORT
Size B	Document Number GA-G41MT-S2PT
Date: Thursday, July 12, 2012	Rev 2.12
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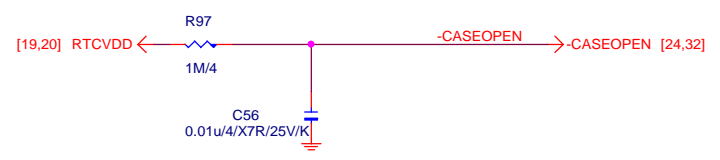
TEMP H/W MONITOR



VOLTAGE-- H/W MONITOR



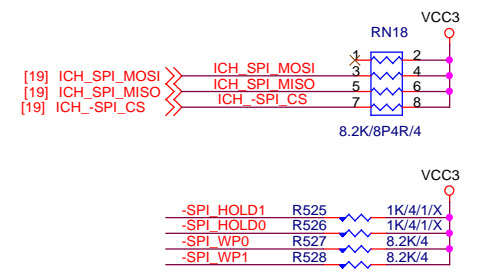
CASE OPEN



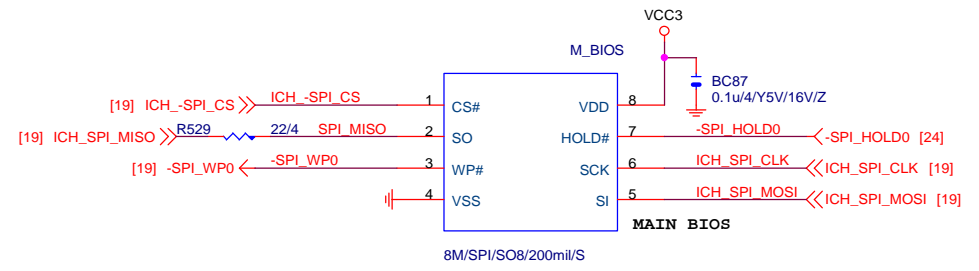
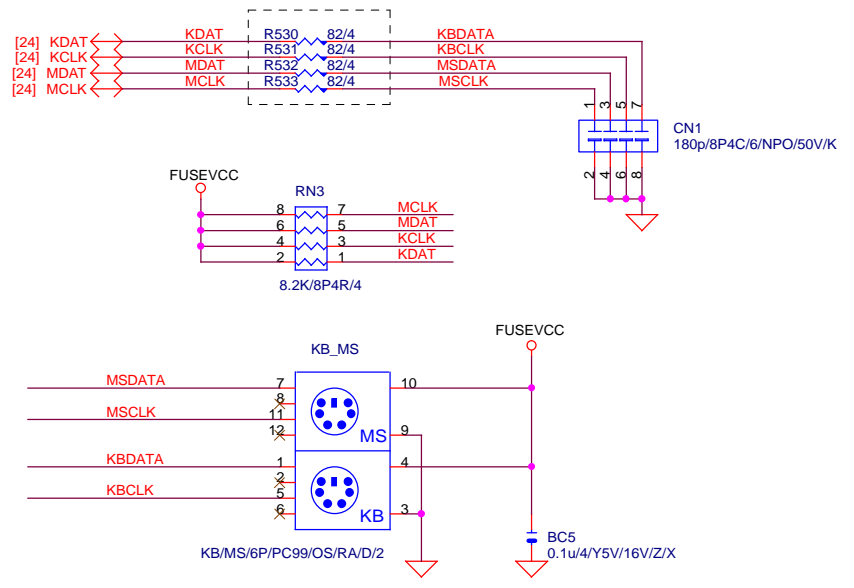
Case Open Circuits

DUAL BIOS

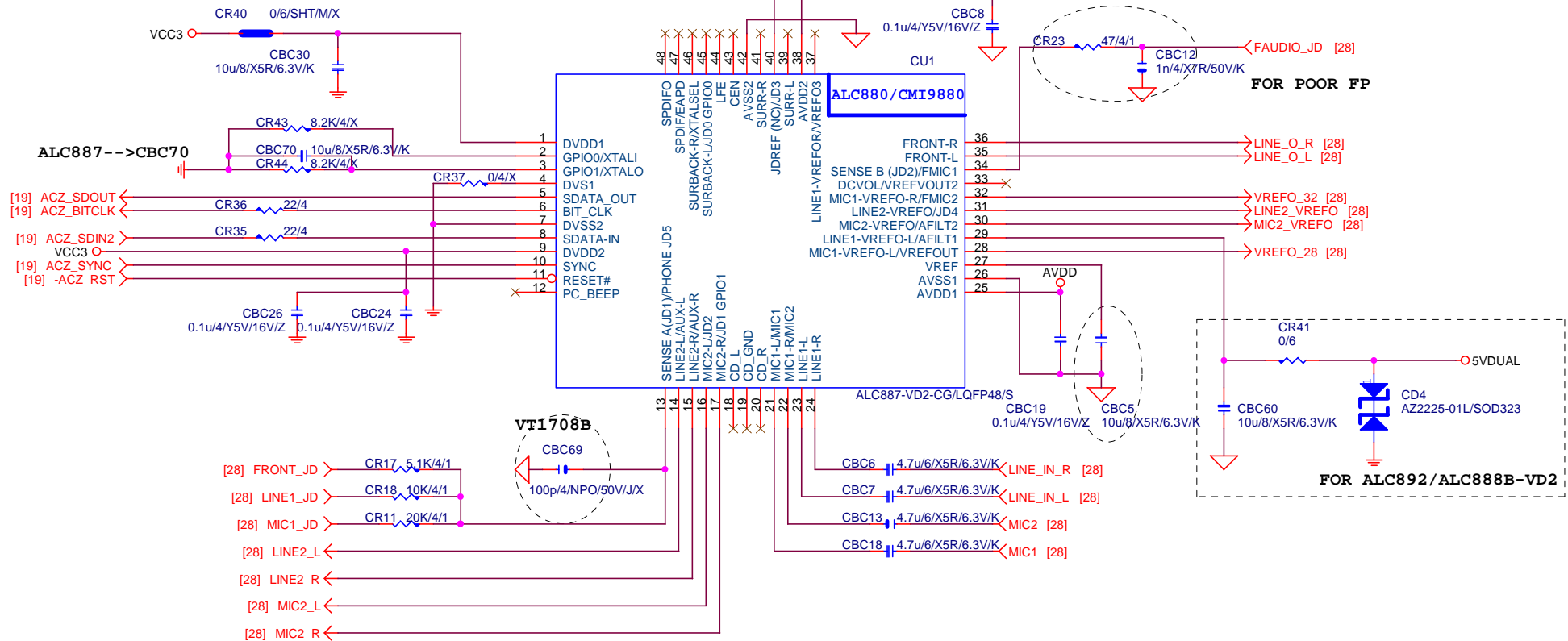
(NEW HOLD# DESIGN)



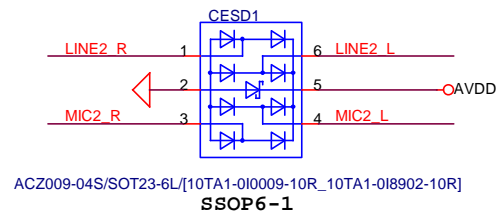
KB/MS



CO-LAY
 ALC888B/ALC892R/ALC892/ALC887-VD/ALC889
 VT1708S/VT2021

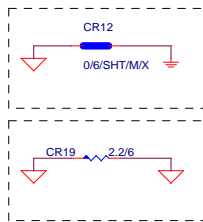
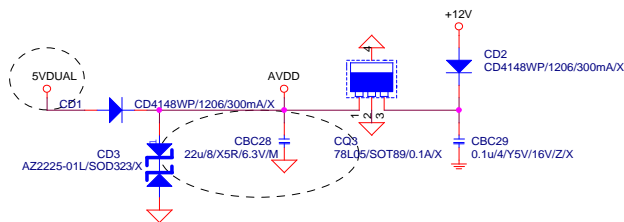


Can Support Amp Out

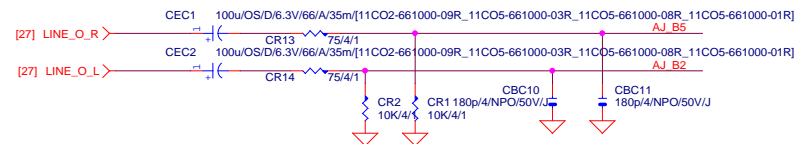


Gigabyte Technology		
Title		
AZALIA ALC887-VD2		
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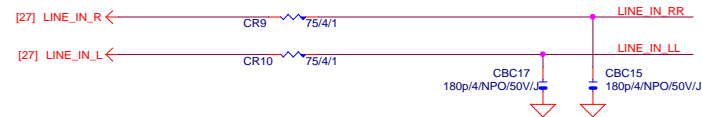
CODEC POWER/EMI PAD



LINE-OUT



LINE-IN



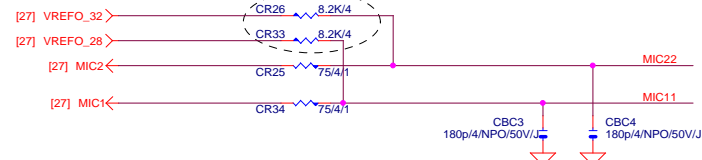
CD IN

N/A

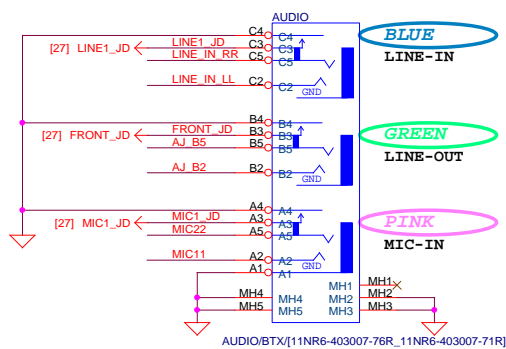
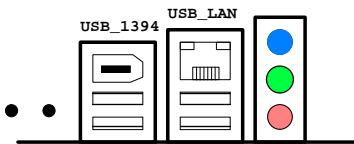
SPDIF IN

N/A

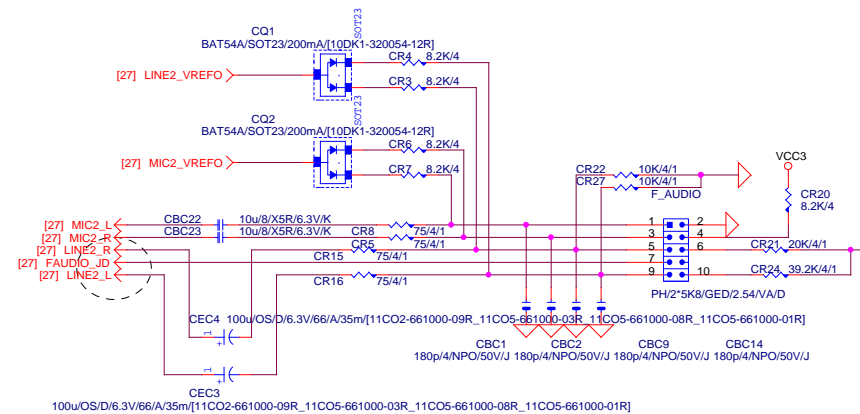
MIC-IN



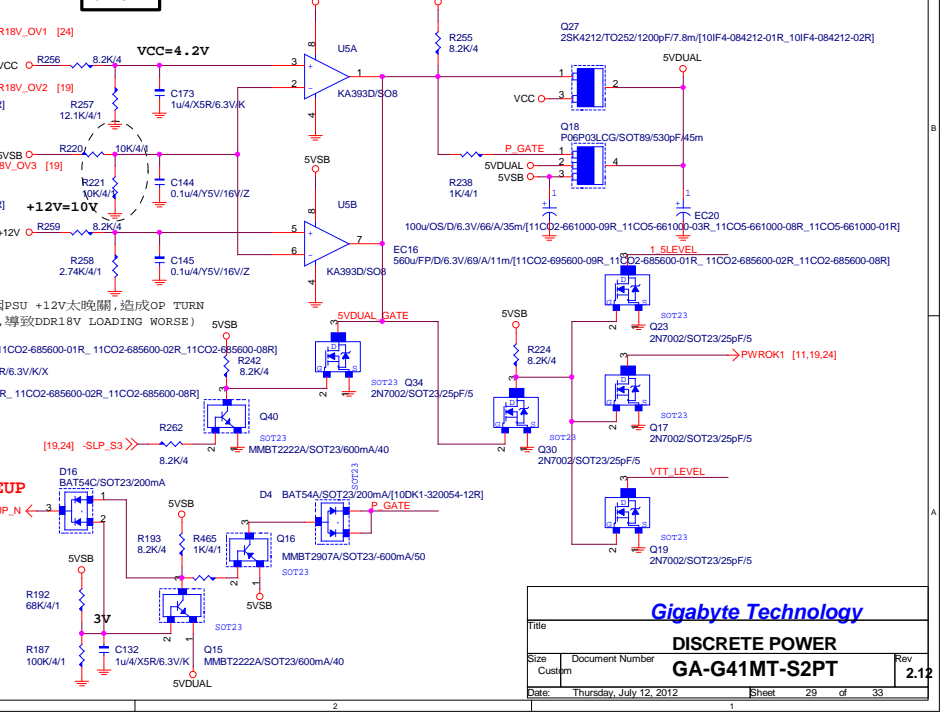
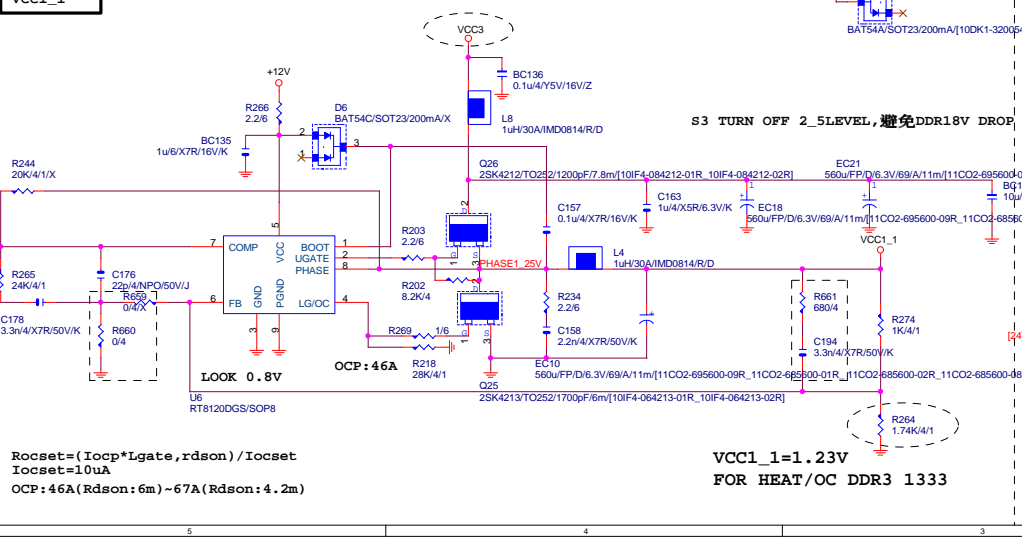
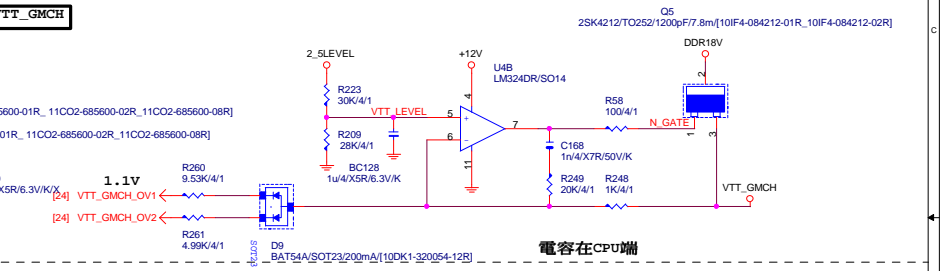
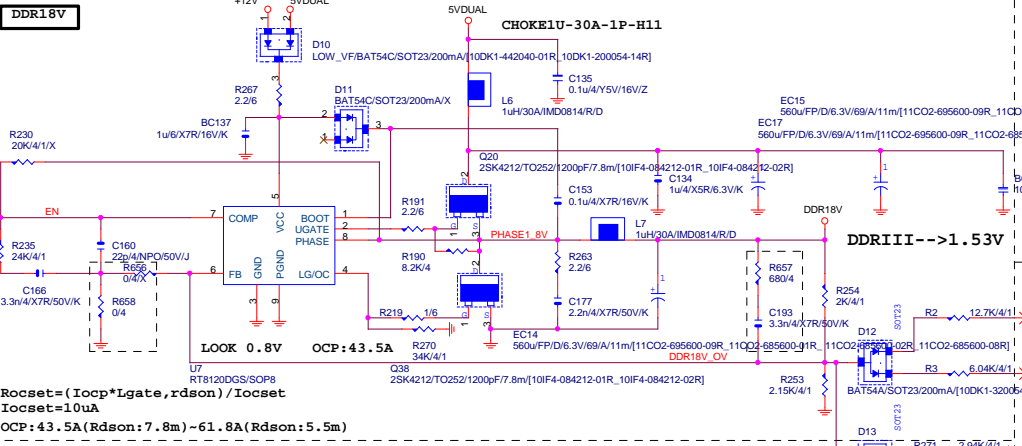
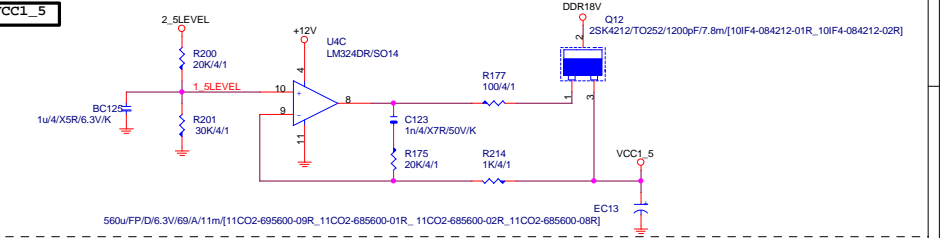
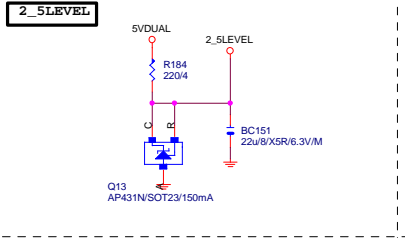
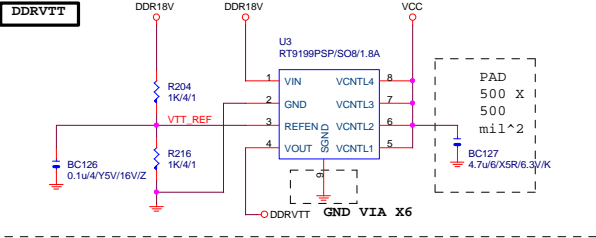
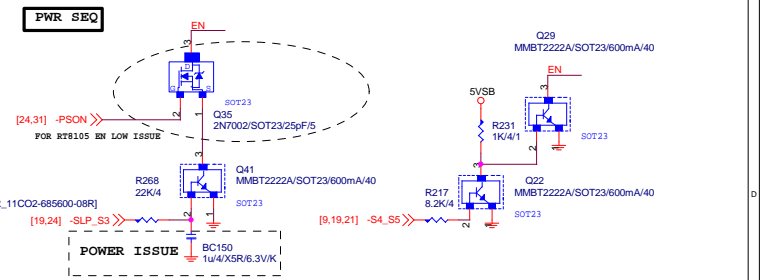
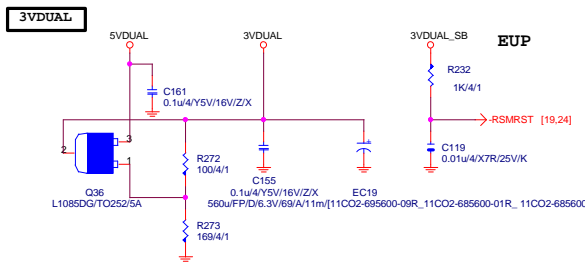
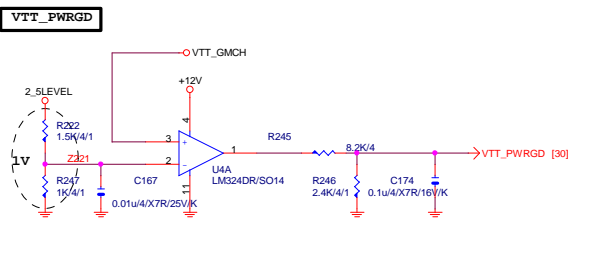
AZALIA JACK



AZALIA FRONT PANEL

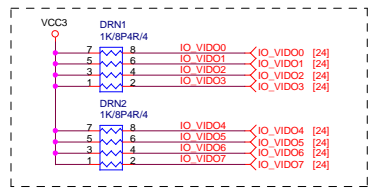
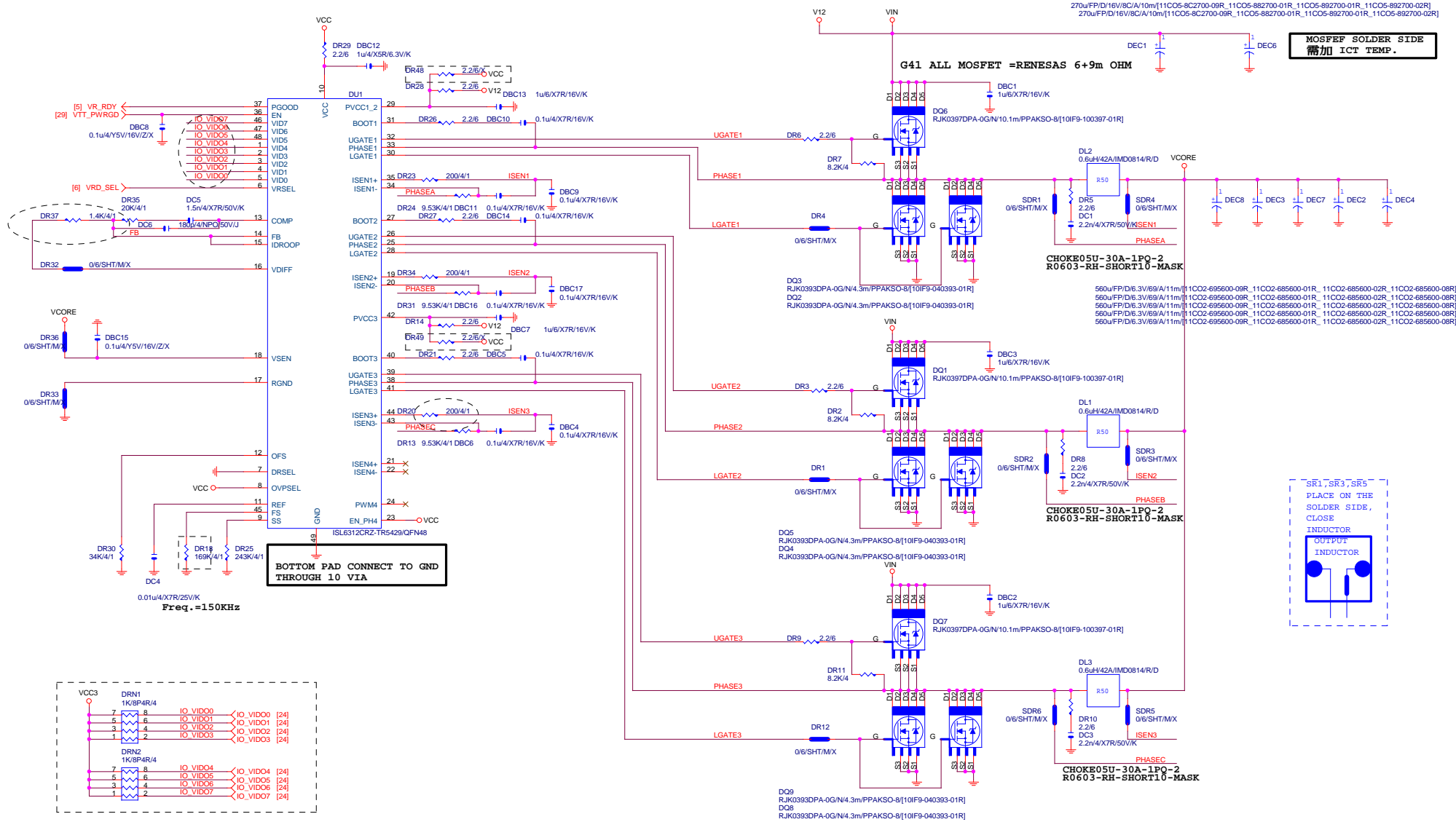


Gigabyte Technology		
AUDIO JACK		
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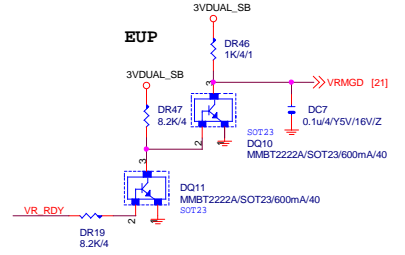
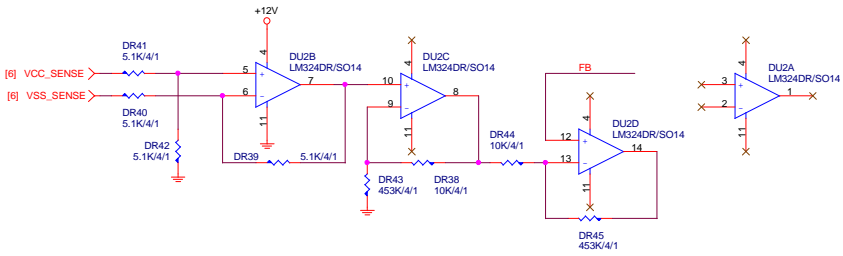


Gigabyte Technology		
DISCRETE POWER		
File	1	Rev
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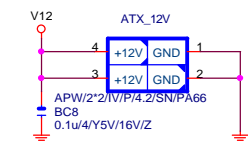
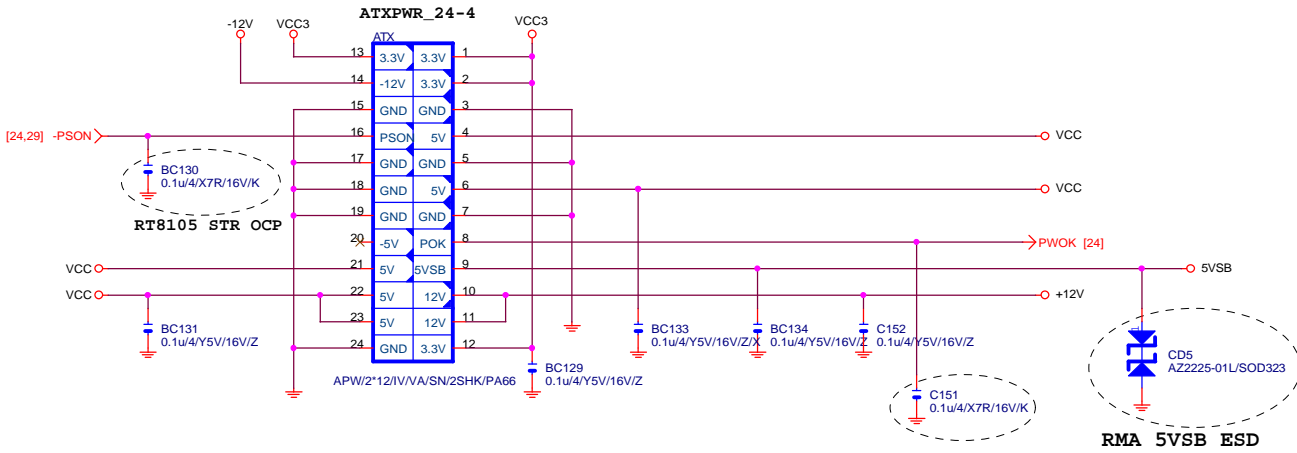
VCORE PWM



CPU VID IT8718: (CPU=I/O=PWM)
IT8720: (CPU->I/O->PWM)



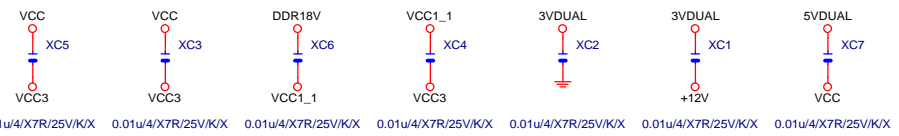
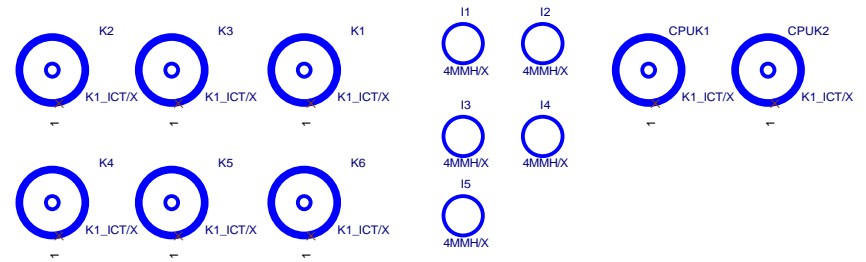
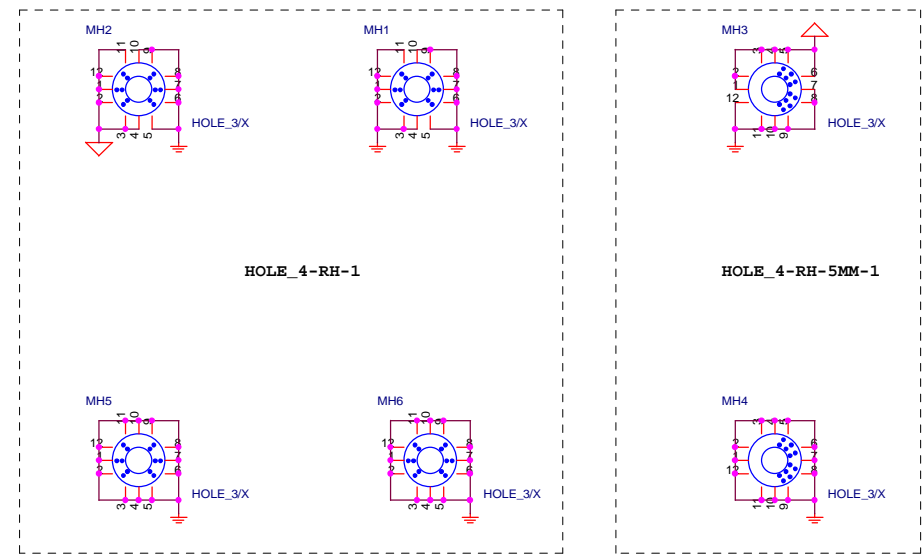
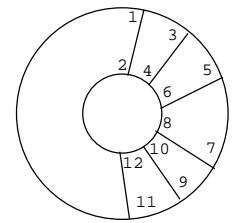
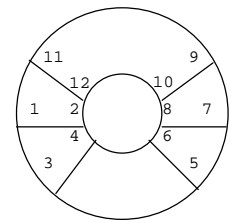
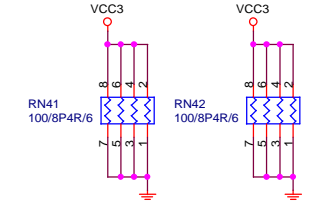
ATX POWER CONNECTOR



ATX_4-5

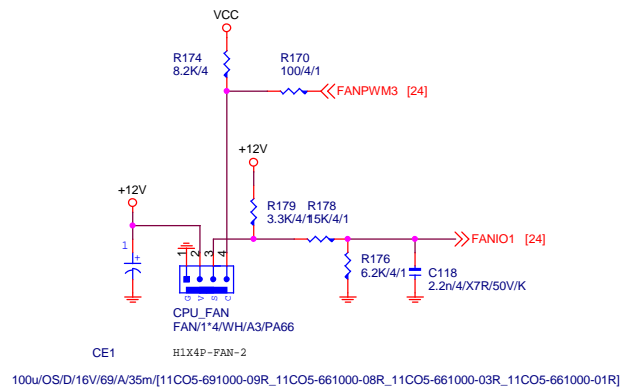
MIN. LOAD

FIX PWR AcBel(ATX-400C-A2ADB)

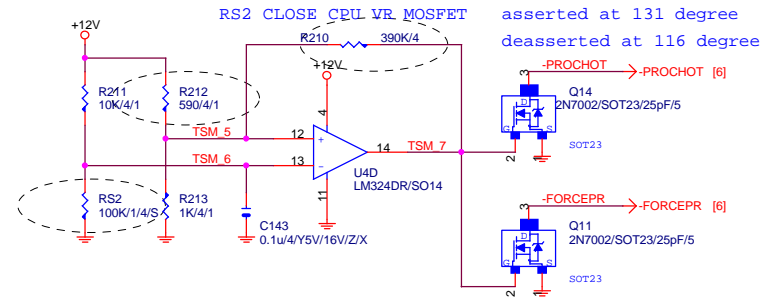


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ATX POWER CONNECTOR		
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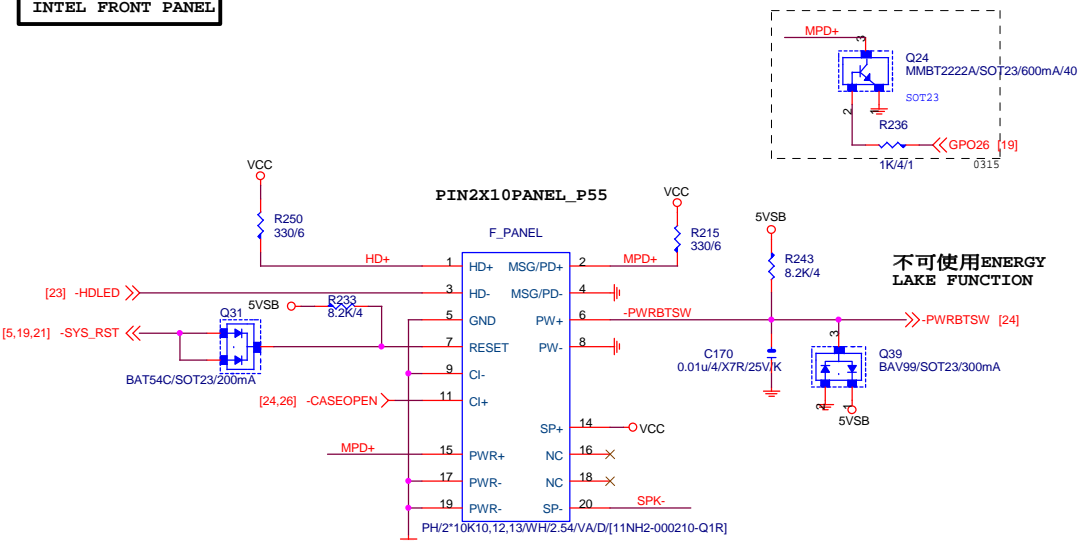
CPU SMART FAN SMART FAN



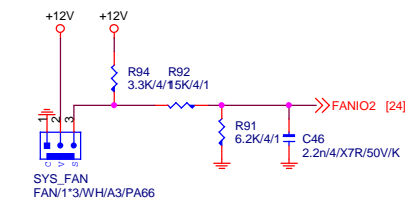
PROCESSOR HOT



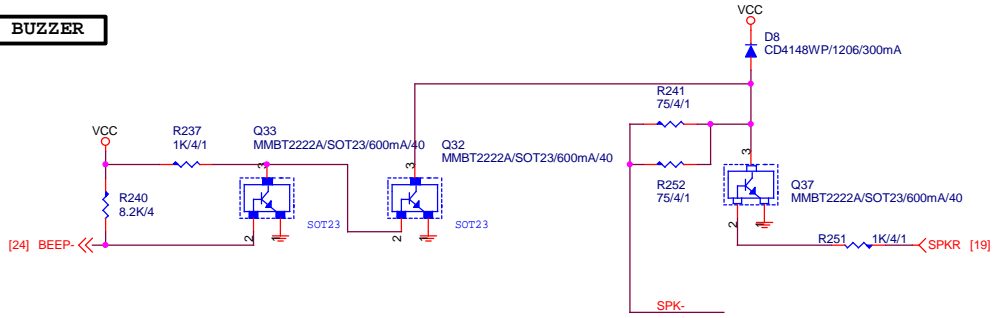
INTEL FRONT PANEL



SYS_FAN



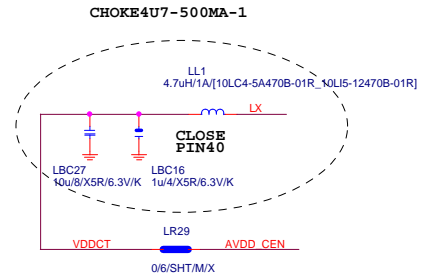
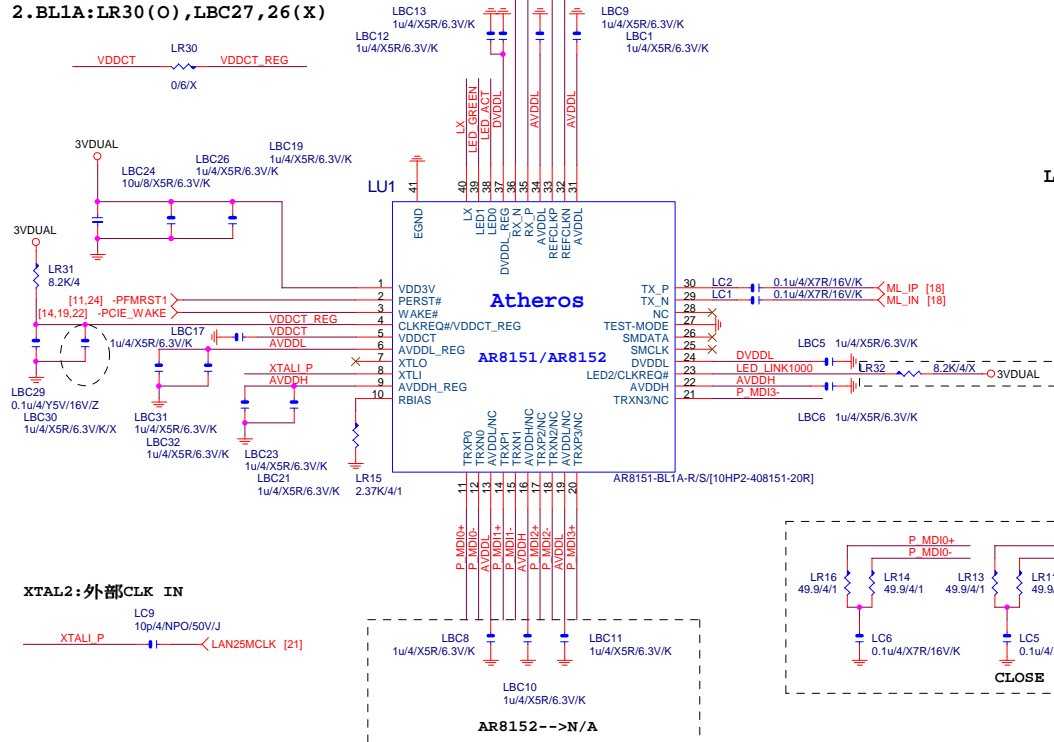
BUZZER



PCIE-1G LAN

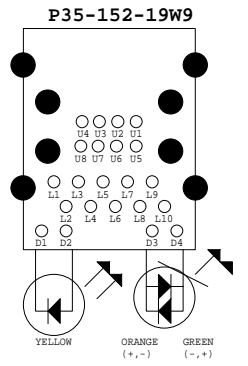
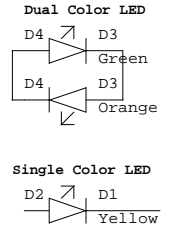
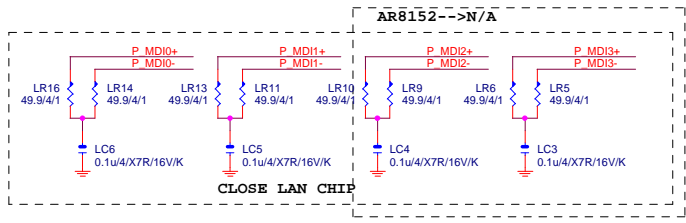
AR8131/AR8132 SWR:
LL1, LBC27, LBC16(O), LR30(X)

AR8152 LDO OUTPUT(PIN4): LL1(X)
1. AL1A: LR30(O), LBC27, 26(O)
2. BL1A: LR30(O), LBC27, 26(X)

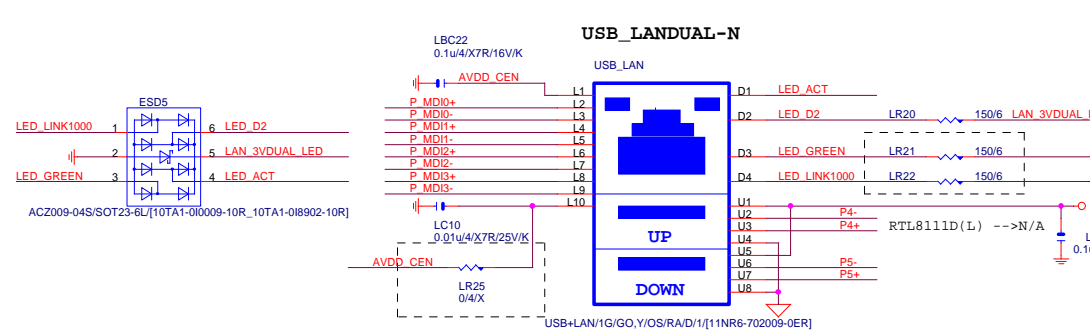


LED0(ACT) LED1(LINK) LED2(LINK1G)

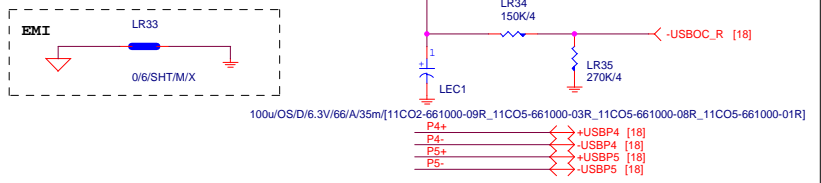
CLKREQ# PULL-UP FOR EFUSE:
1. AR8151: LR31(O), LR32(X)
2. AR8152: LR31(X), LR32(O)



USB_LAN CONNECTOR



USB_LAN



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